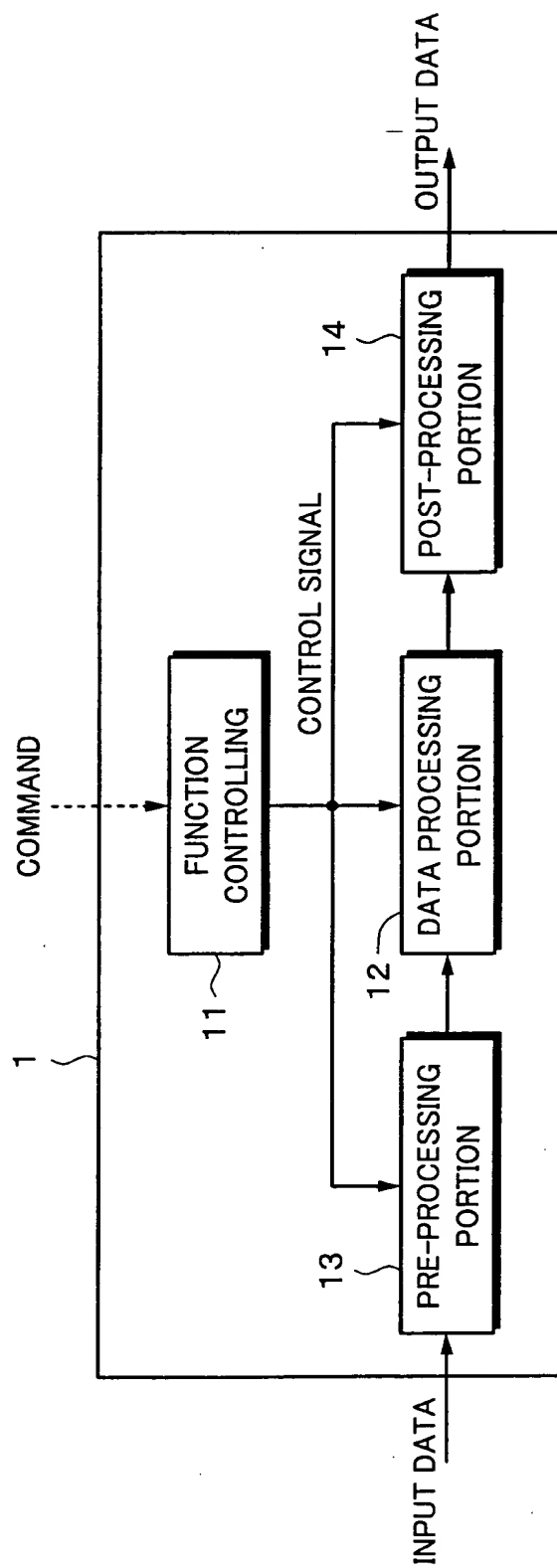


**Fig. 1**

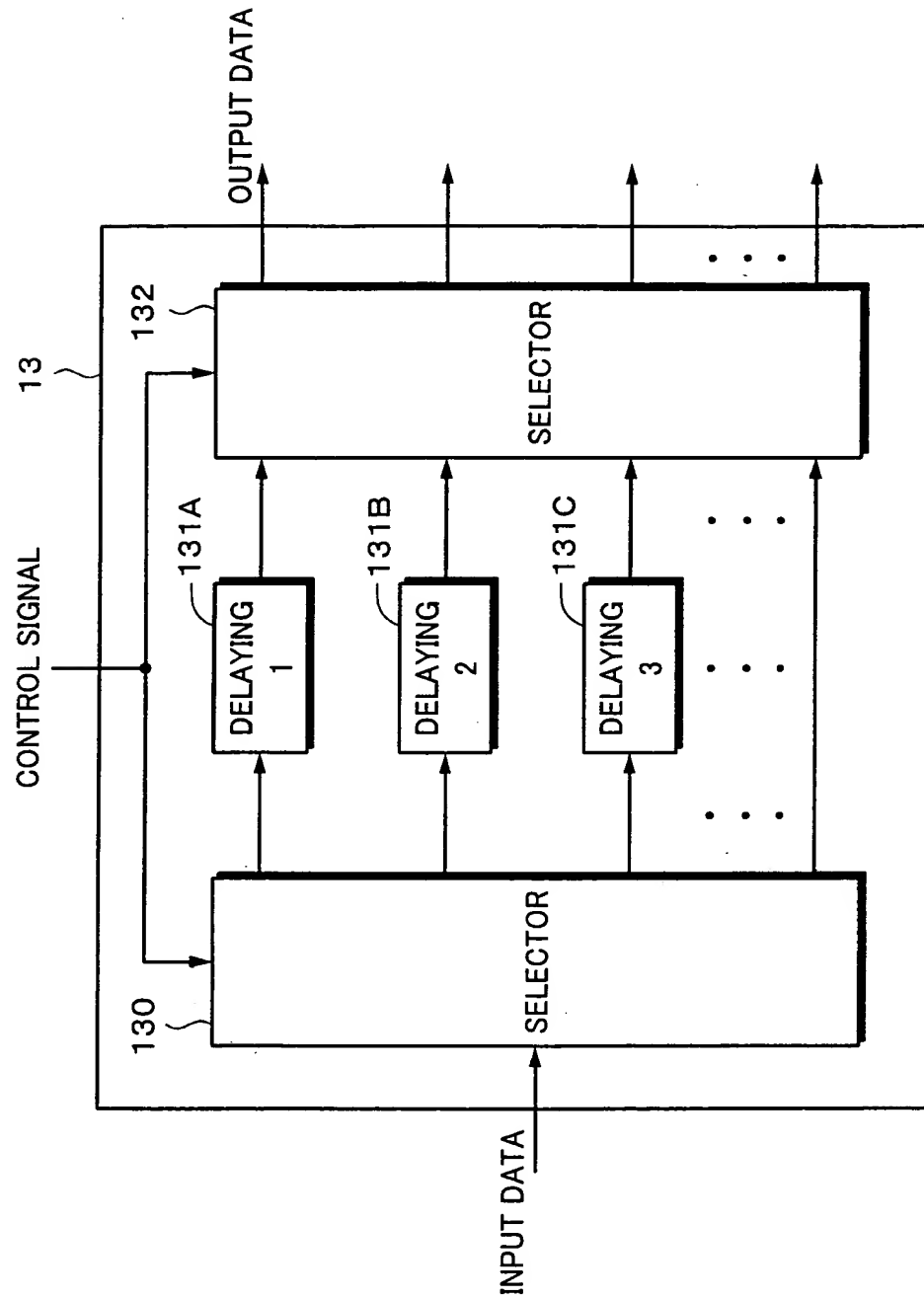
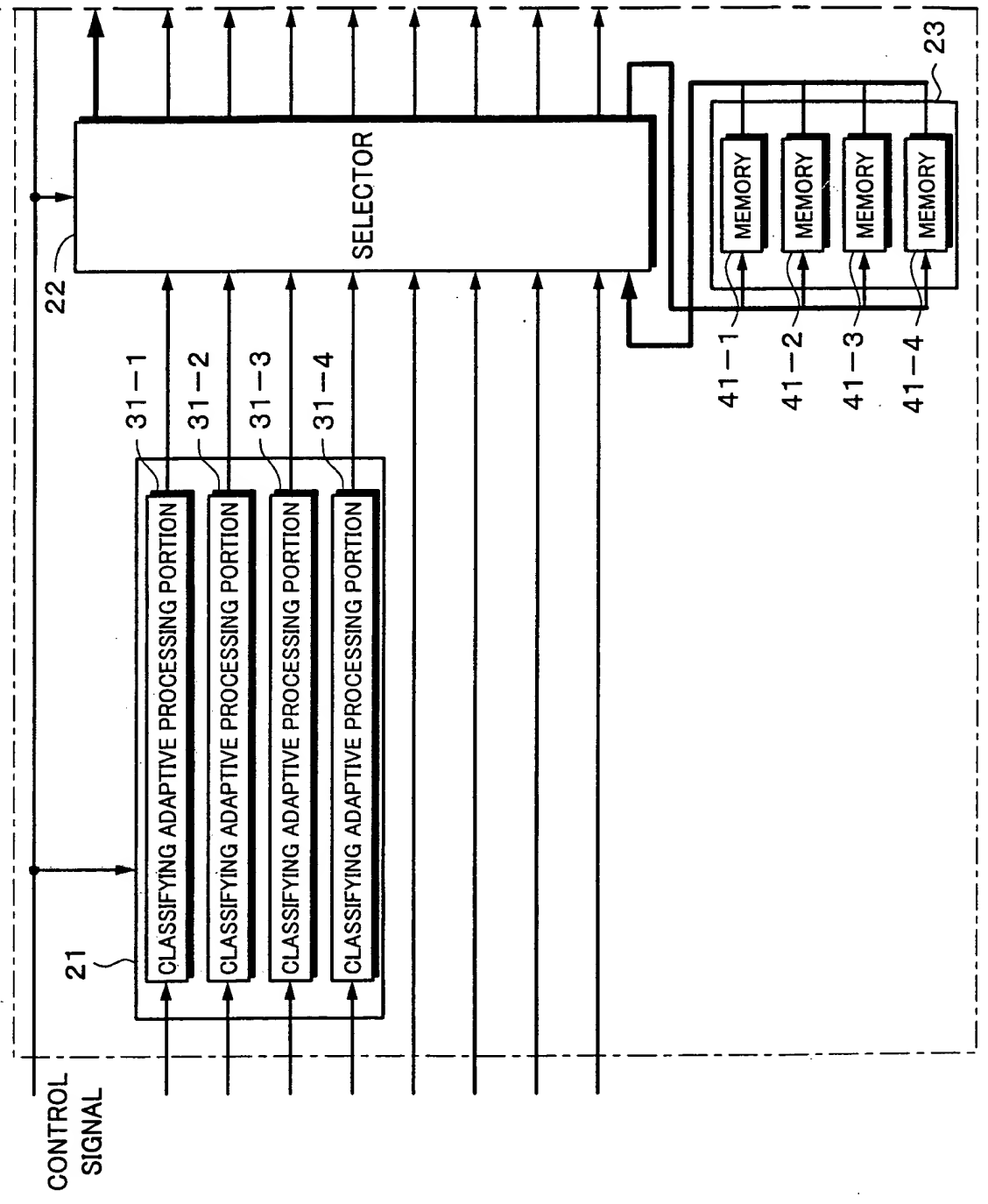
**Fig. 2**

Fig. 3A Fig. 3B

Fig. 3A



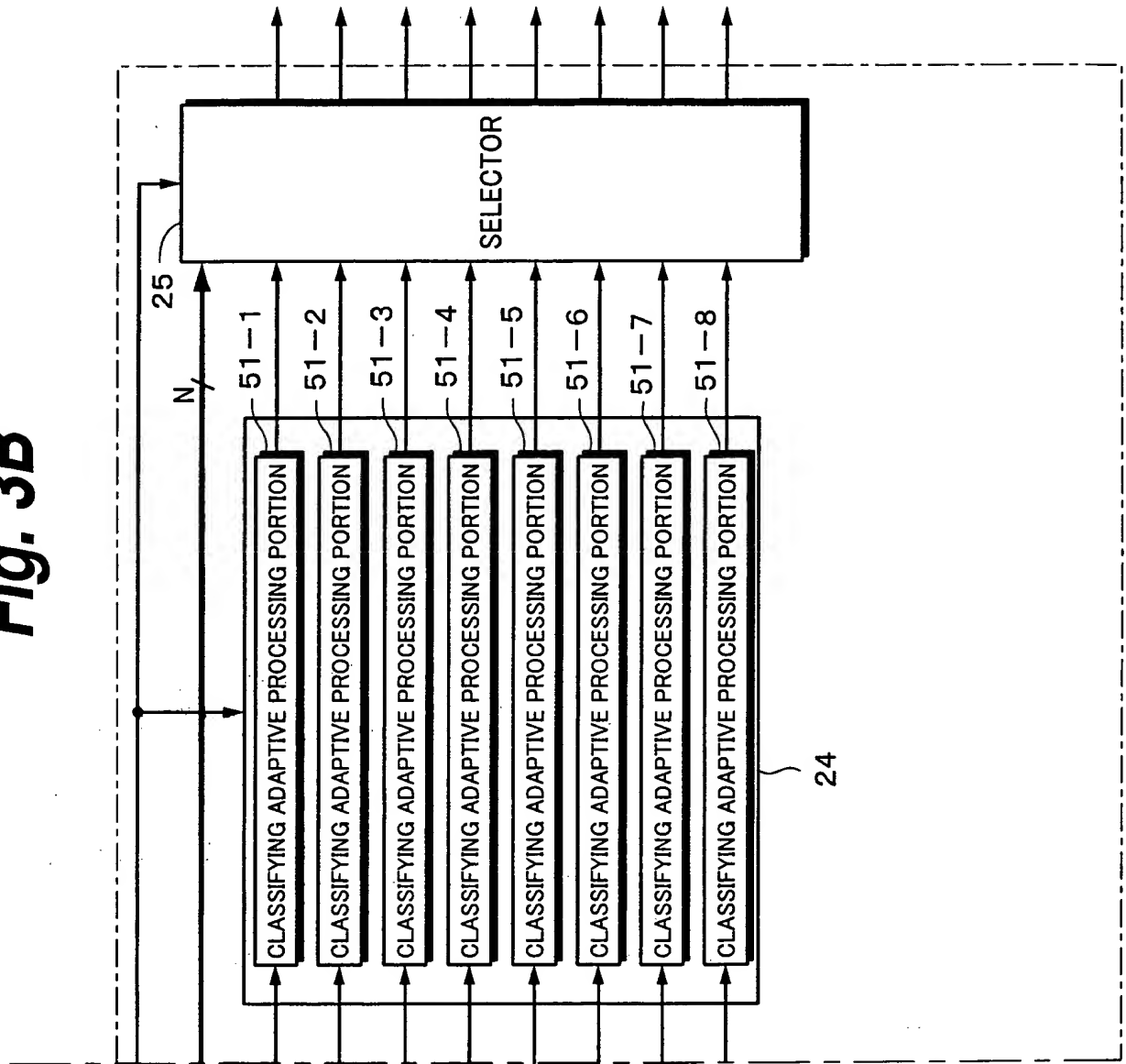
**Fig. 3B**

Fig. 4

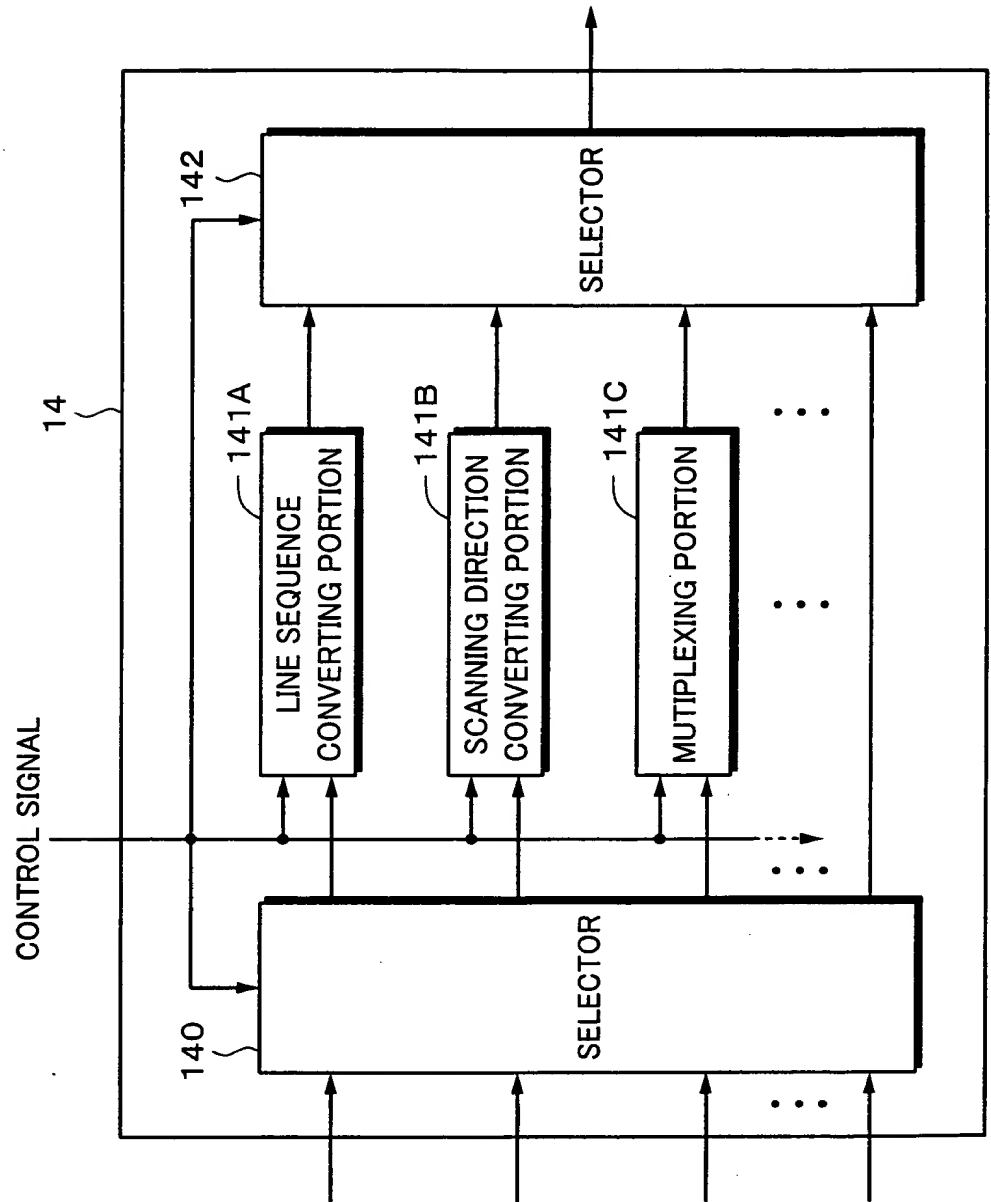
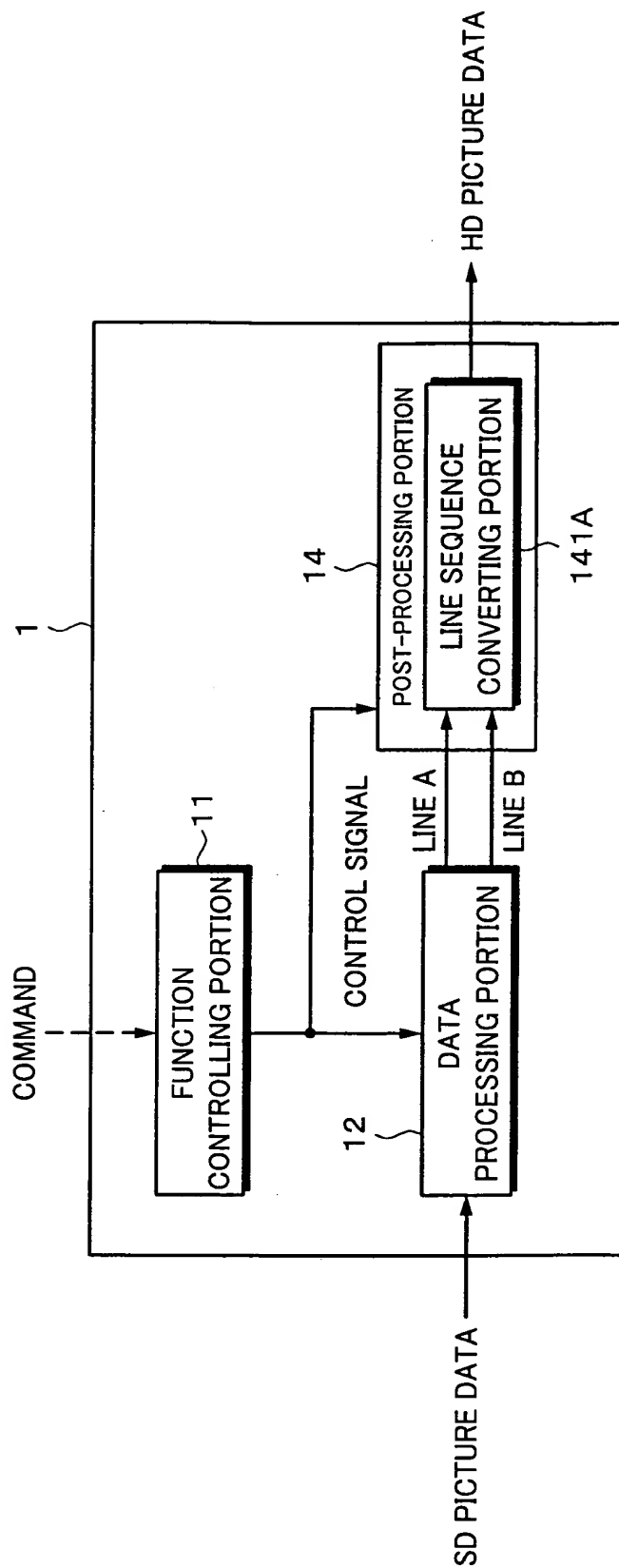
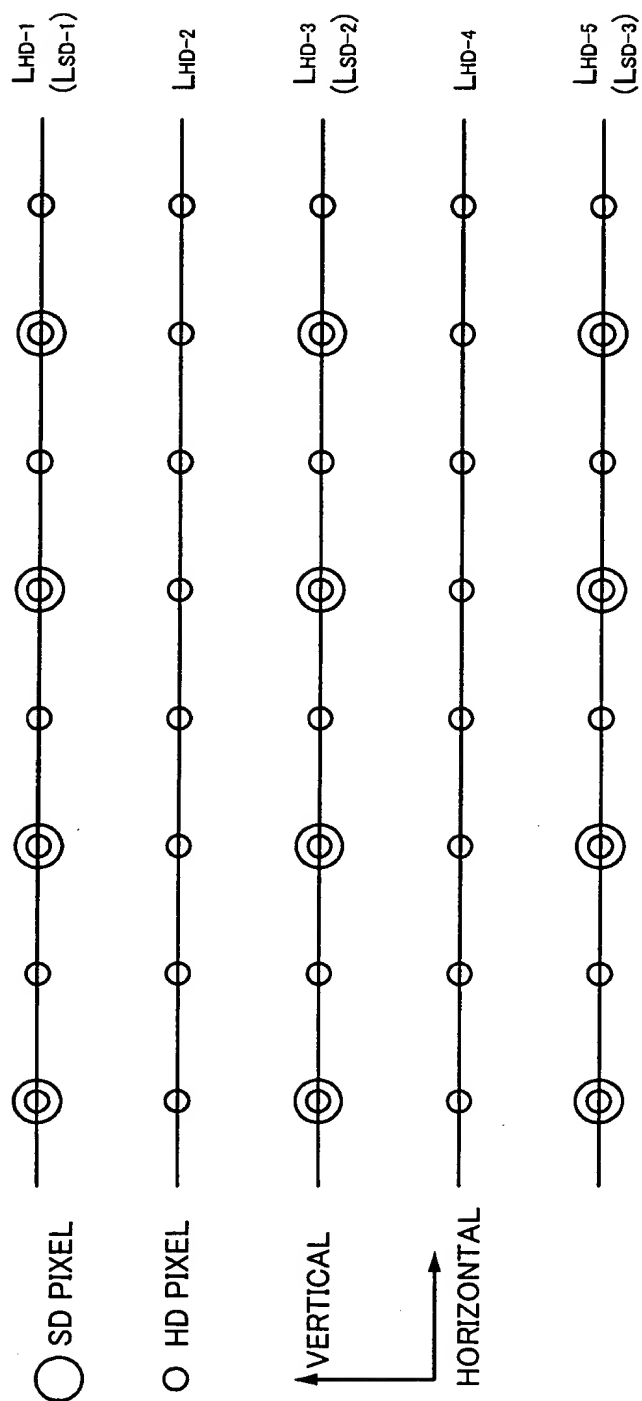
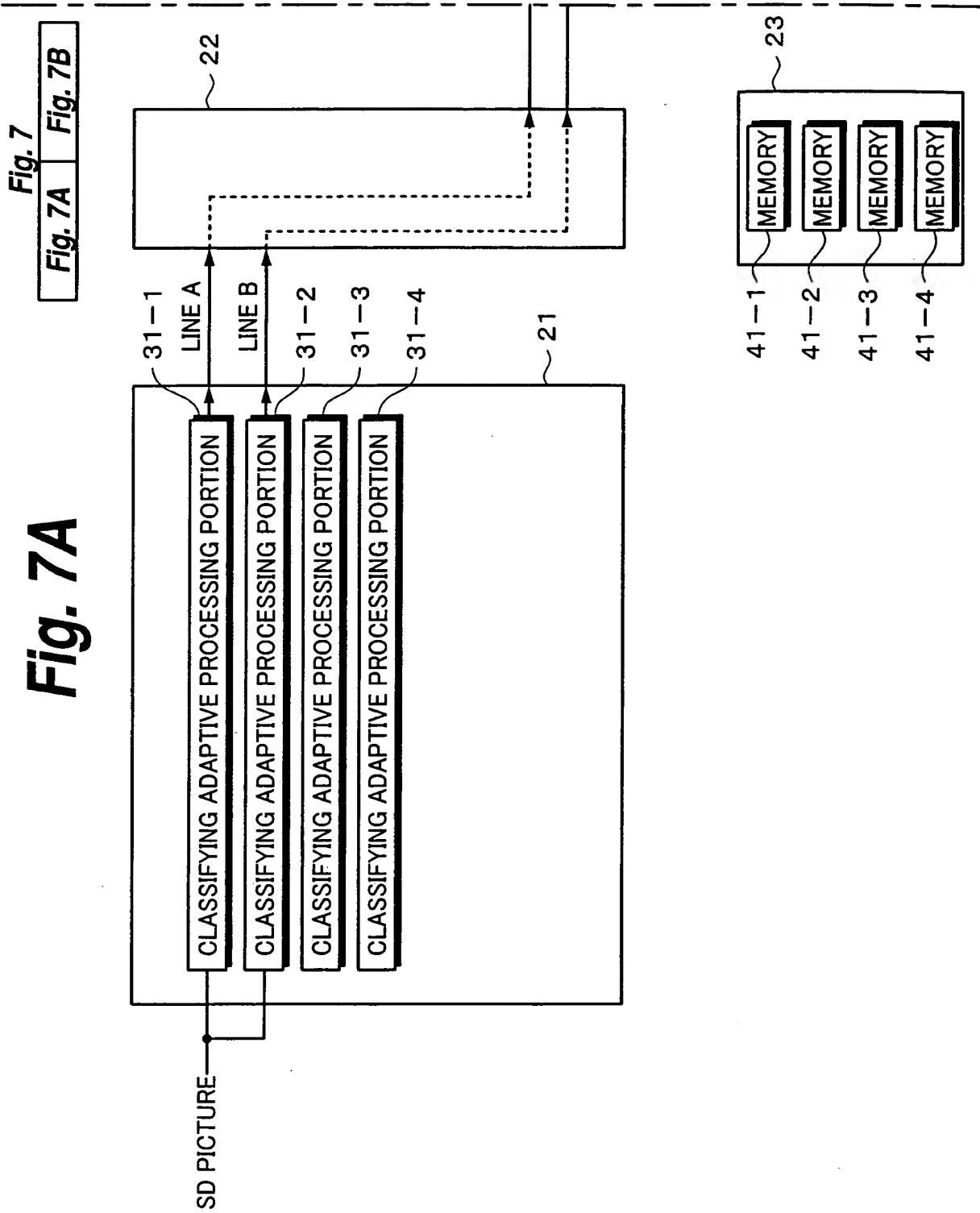


Fig. 5



**Fig. 6**







**Fig. 7B**

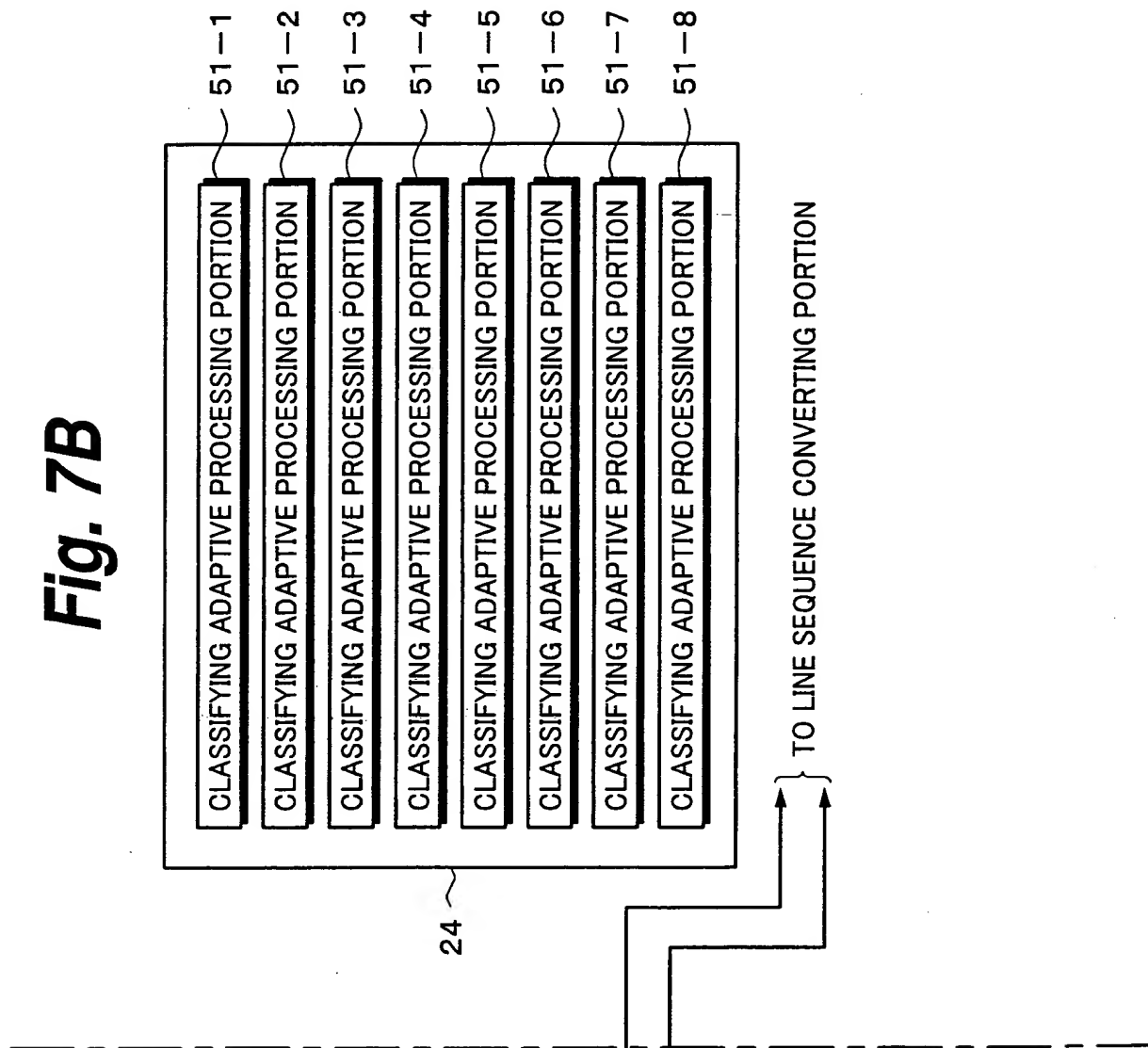


Fig. 8

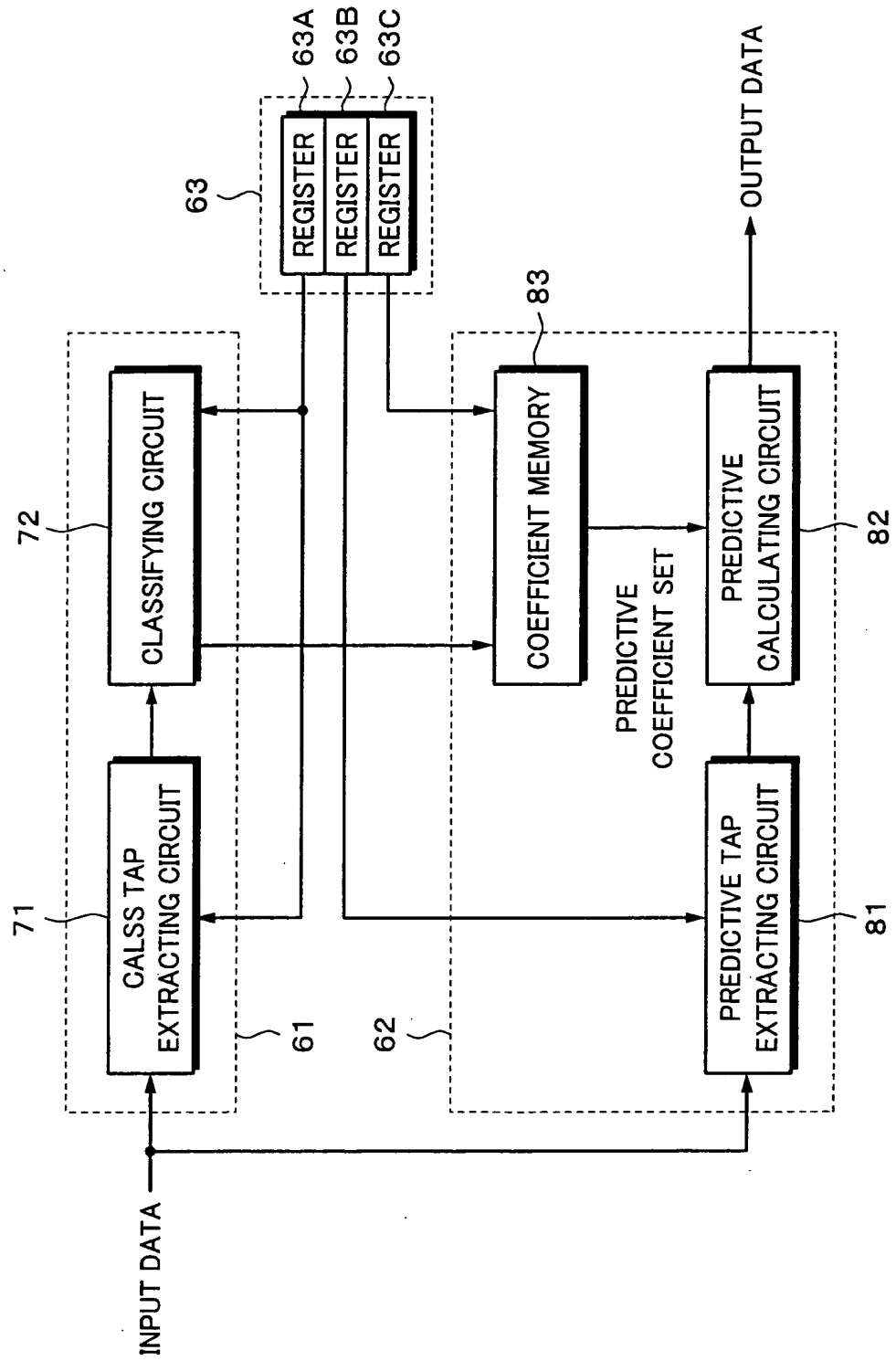
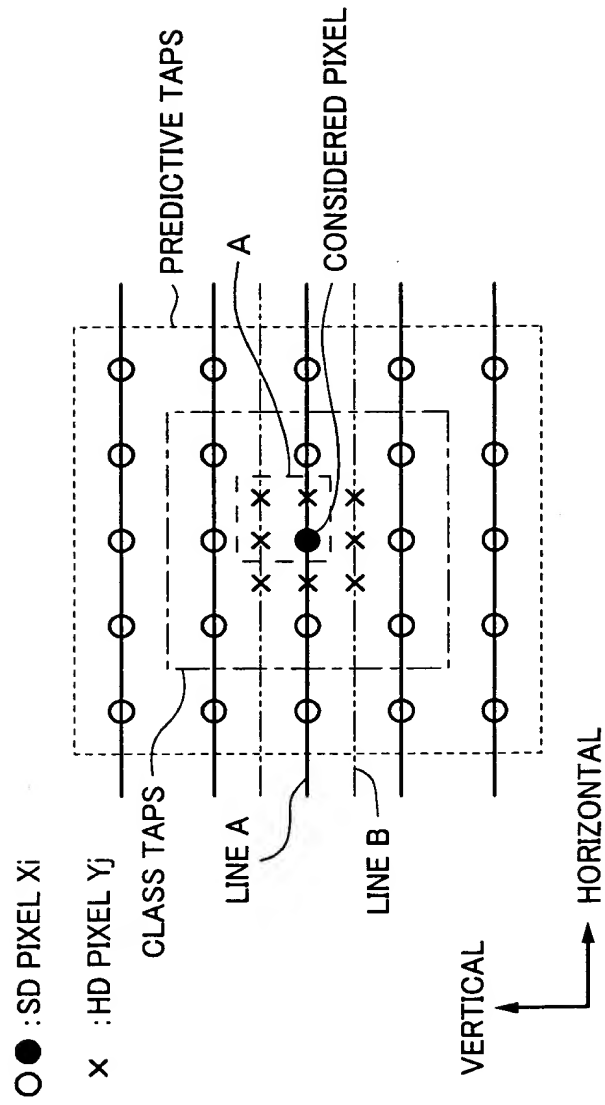


Fig. 9



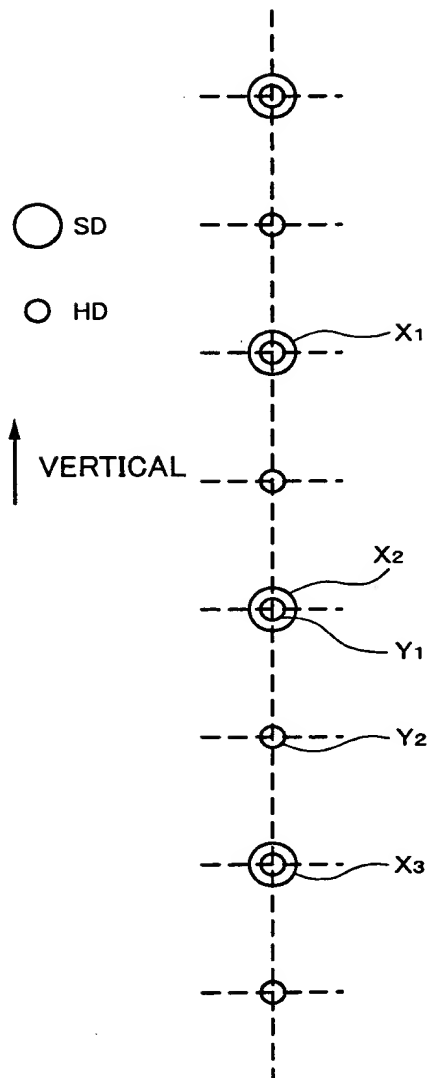
**Fig. 10**

Fig. 11

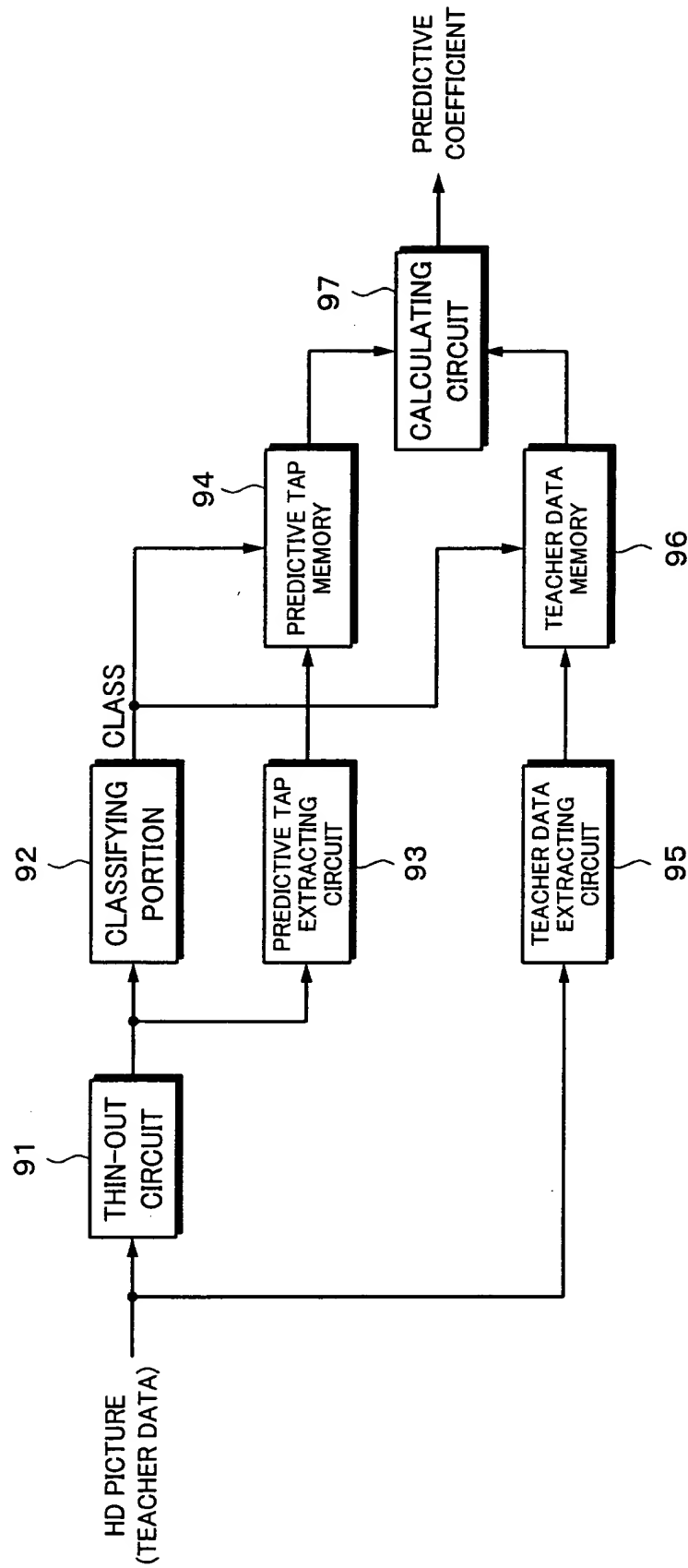
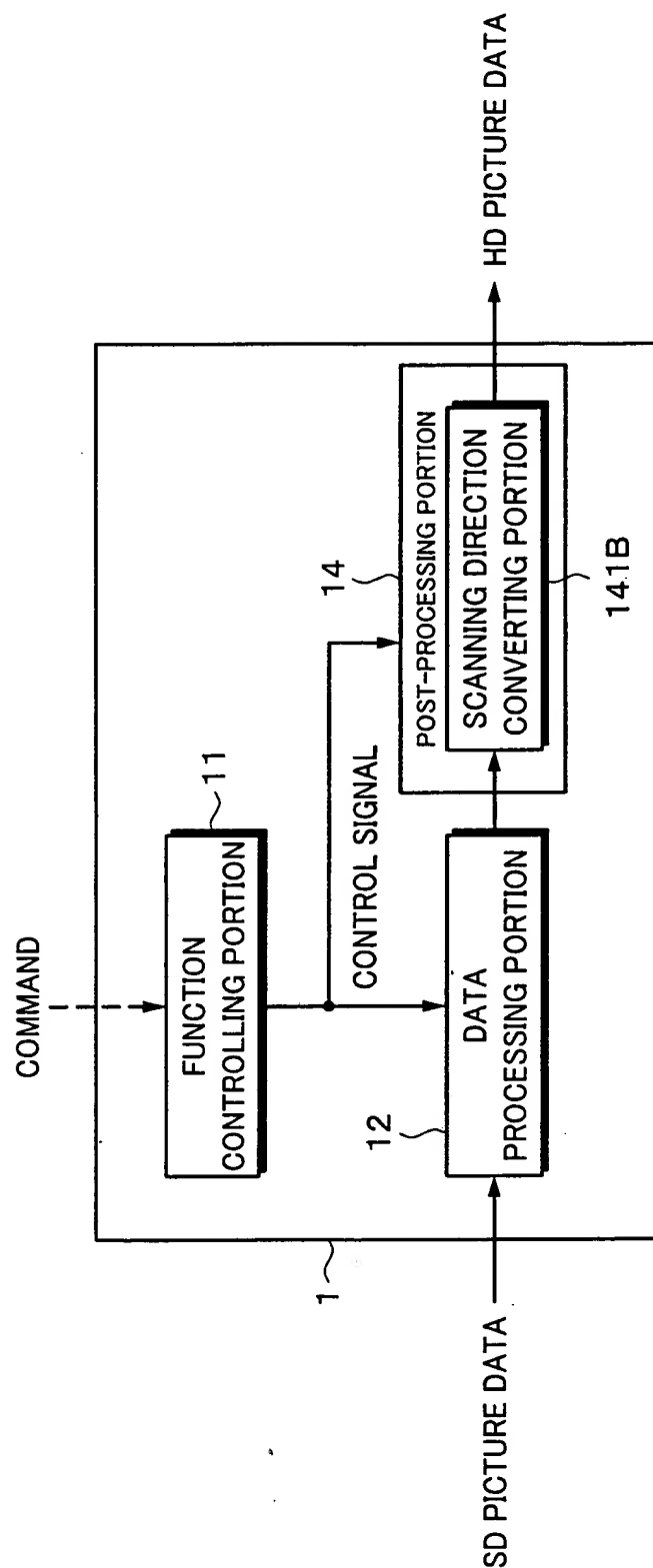
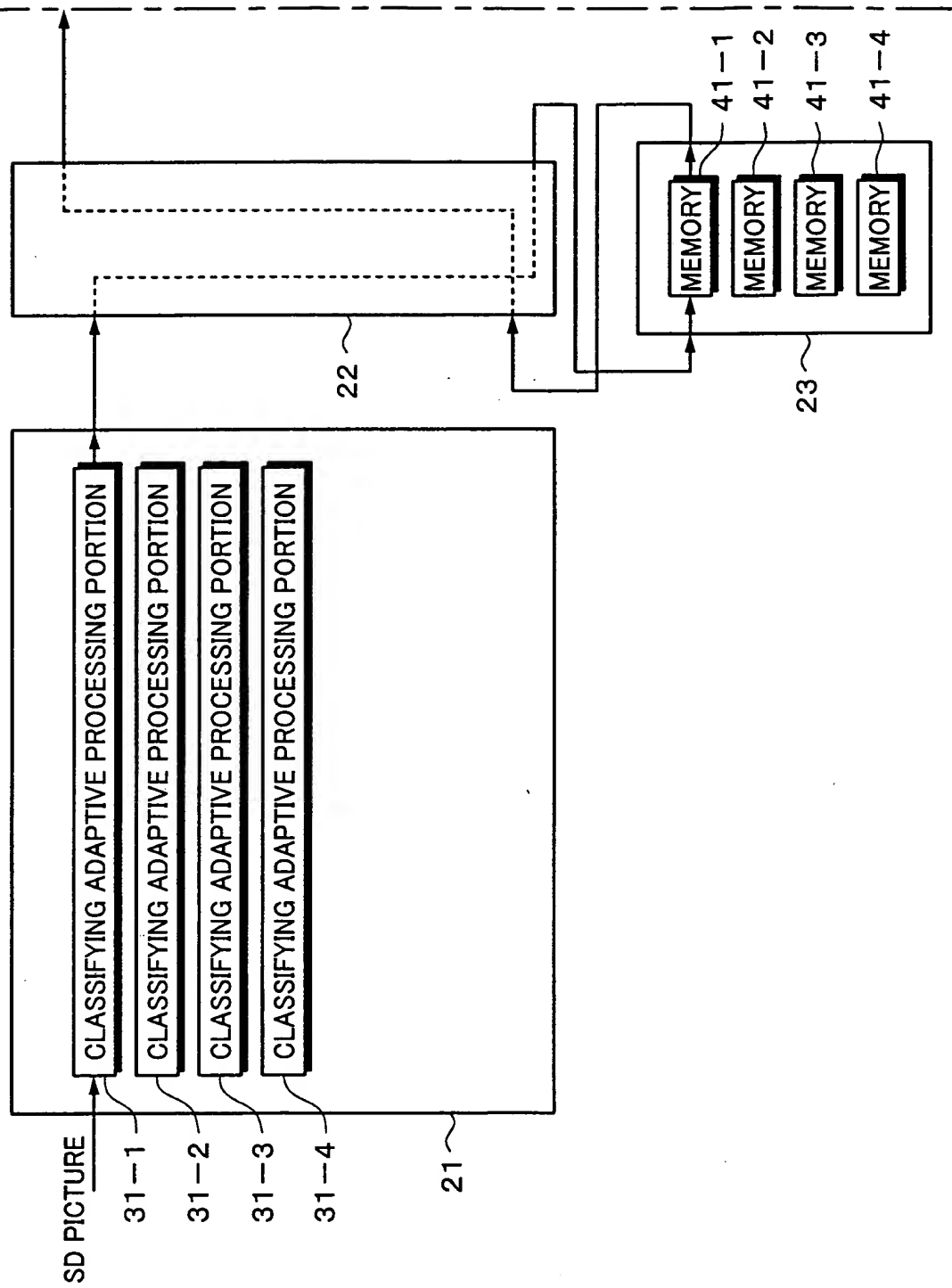


Fig. 12



**Fig. 13A****Fig. 13**  
**Fig. 13A** **Fig. 13B**

**Fig. 13B**

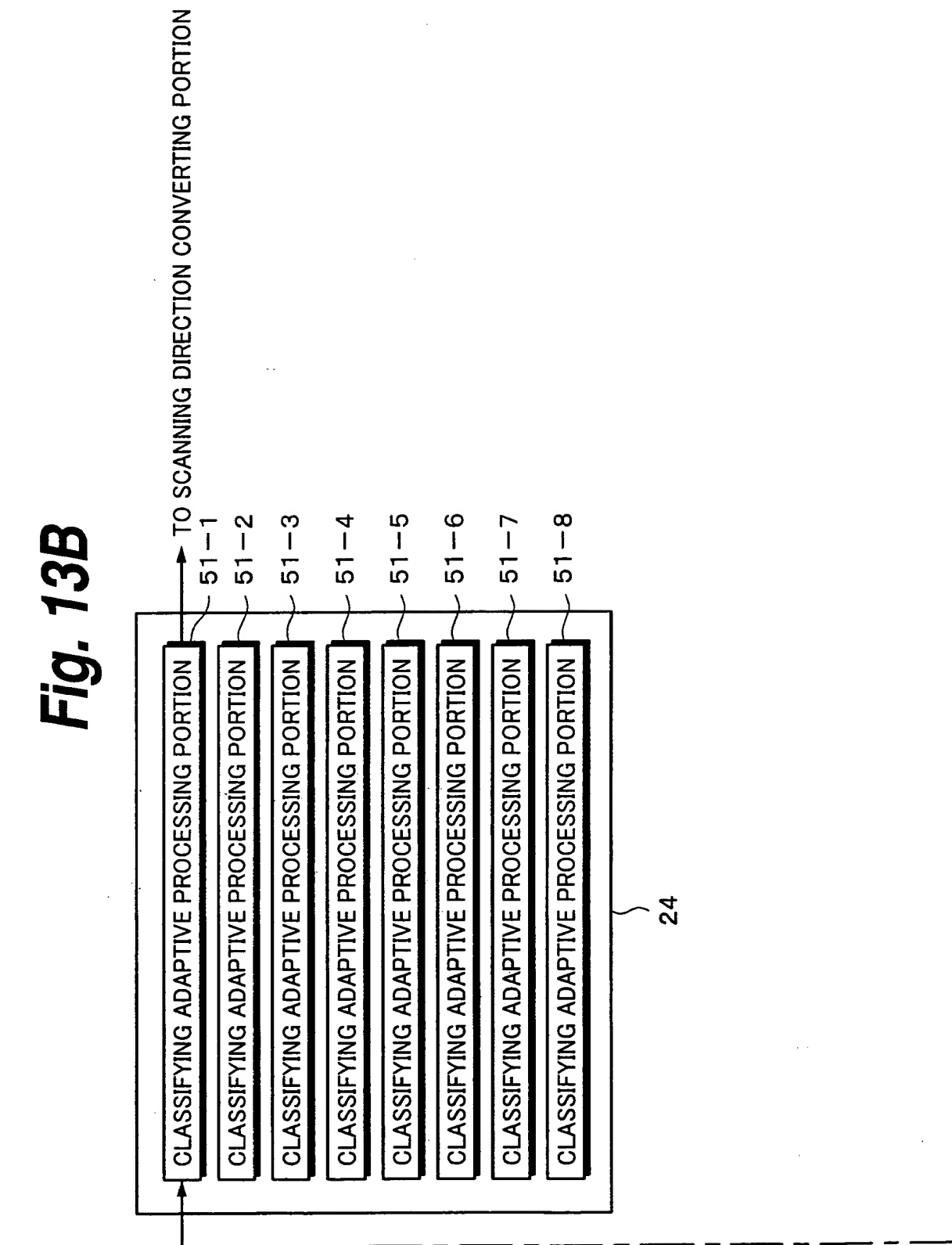






Fig. 15

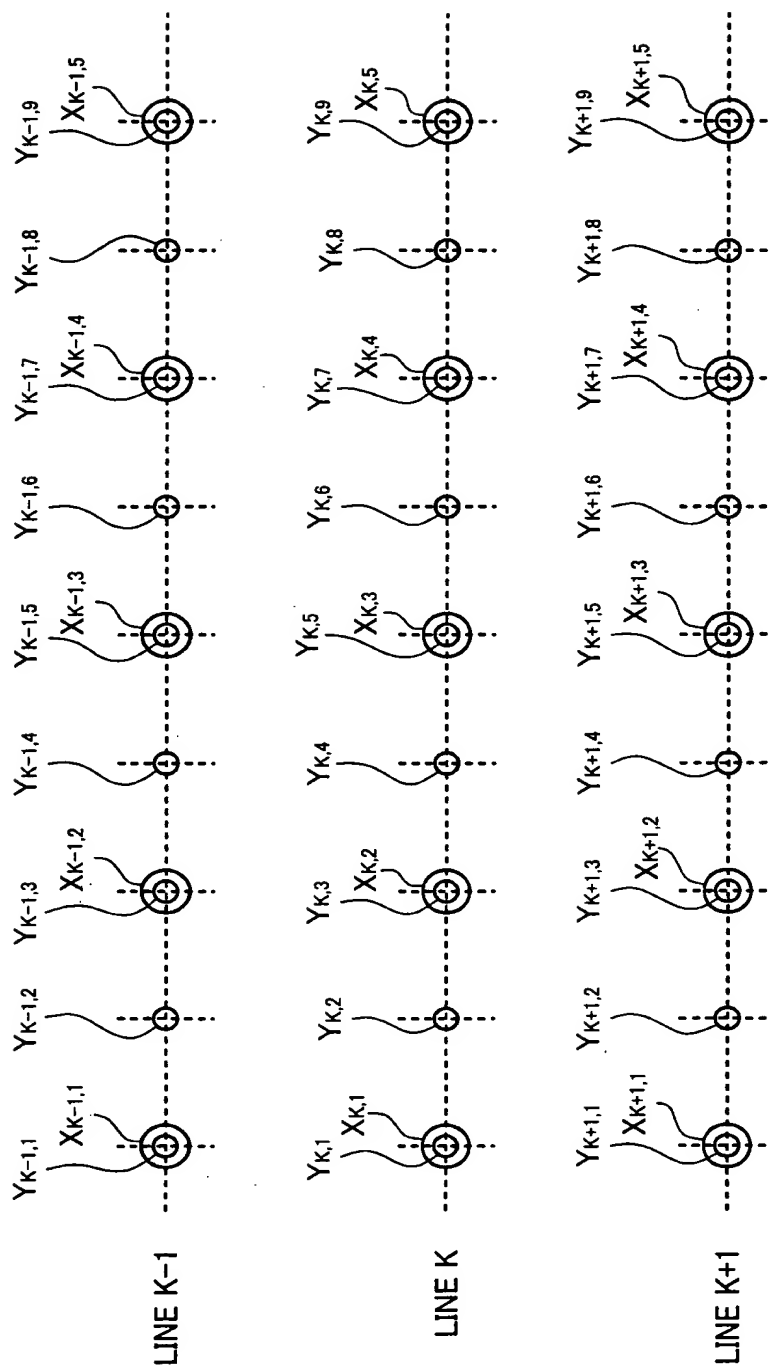
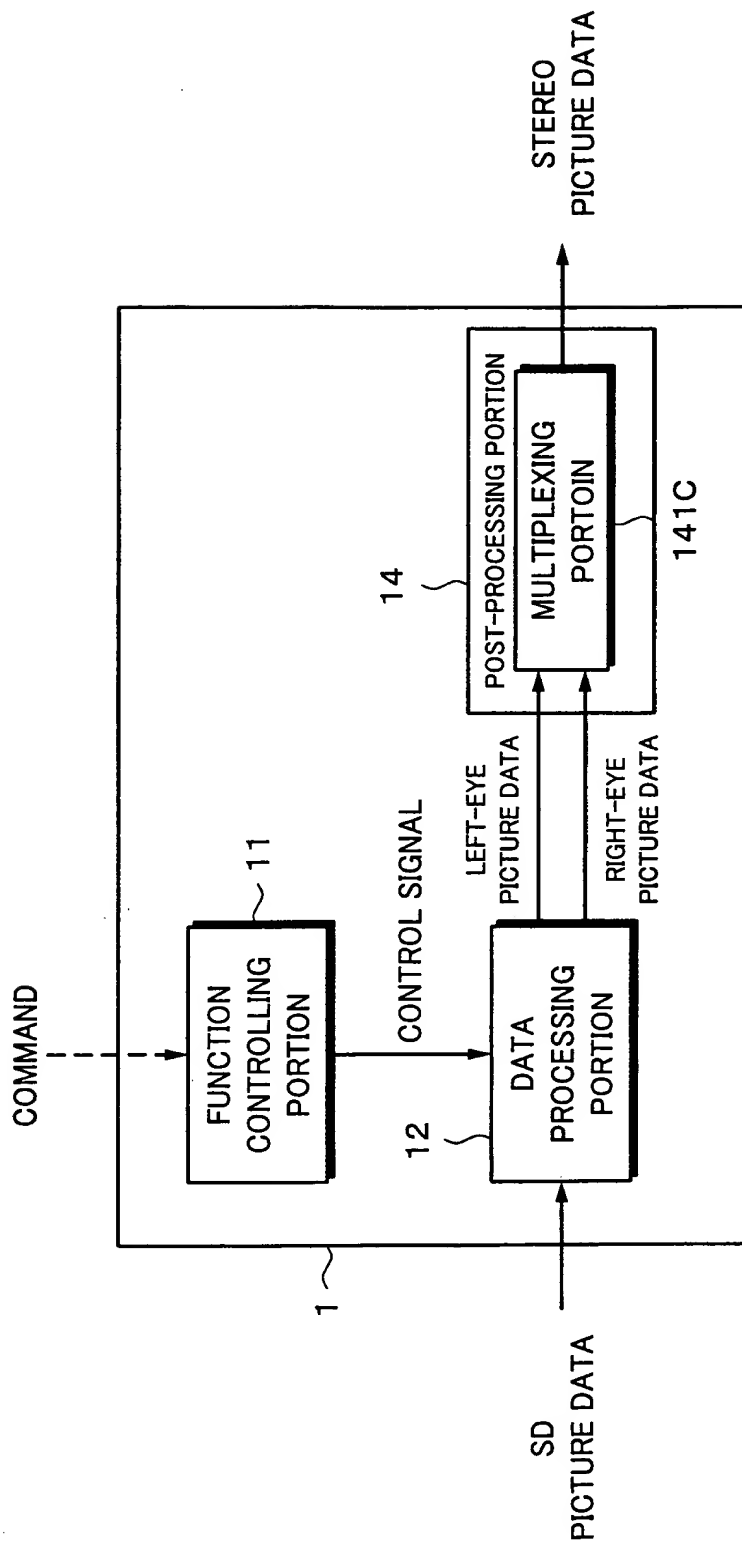


Fig. 16



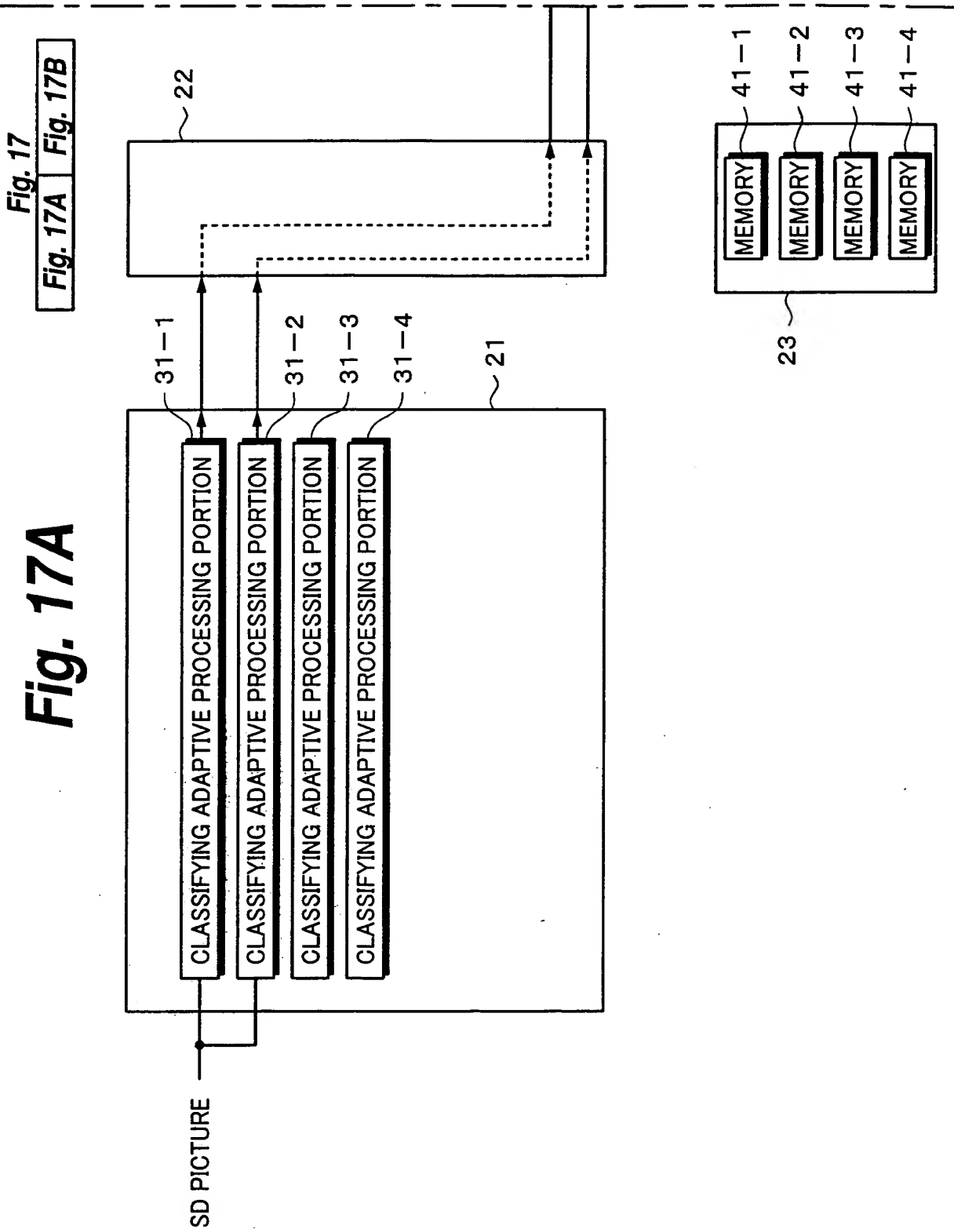


Fig. 17B

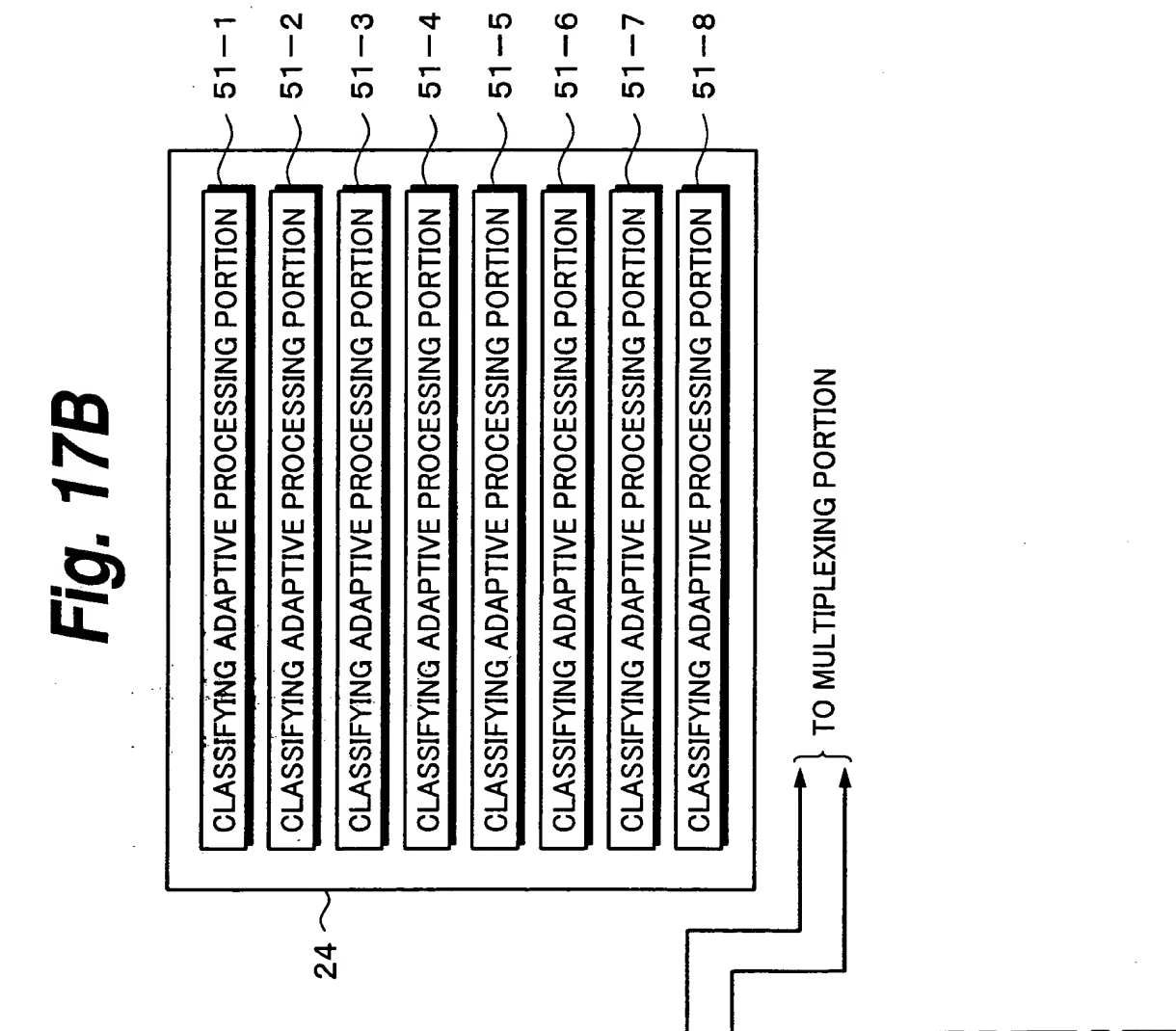
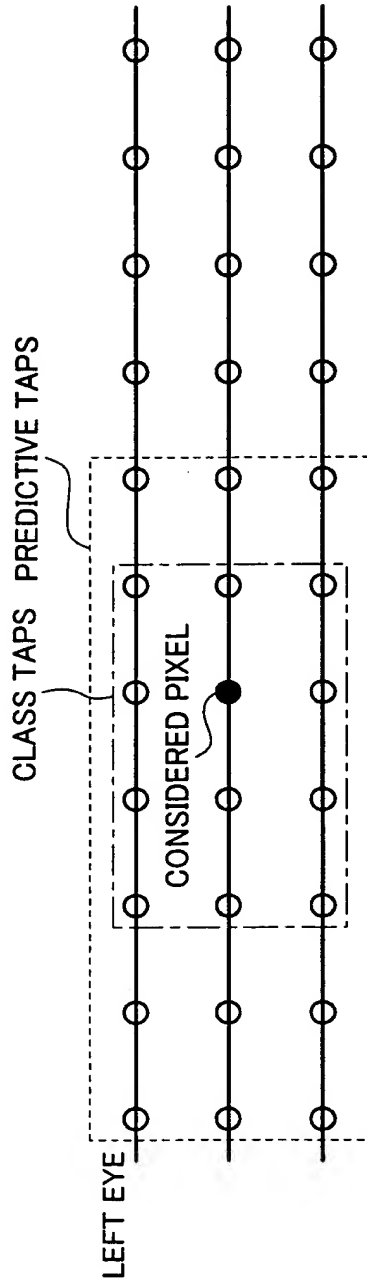
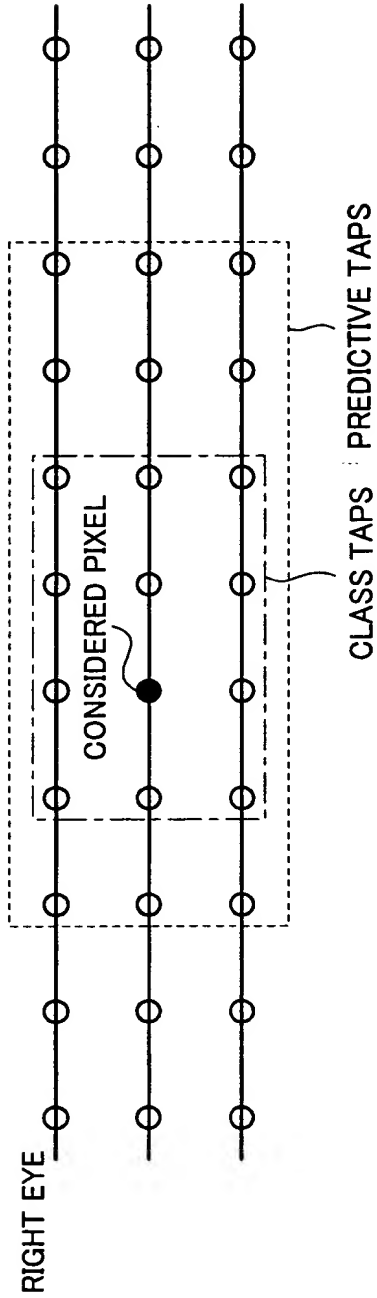


FIG. 18A



**Fig. 18A**



**Fig. 18B**

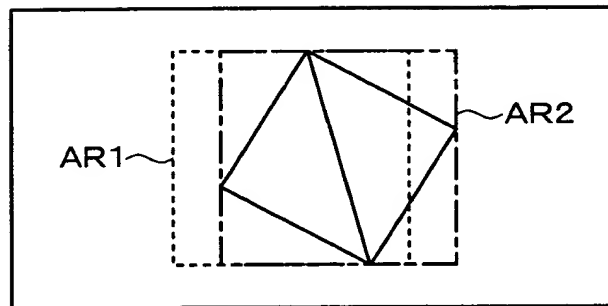
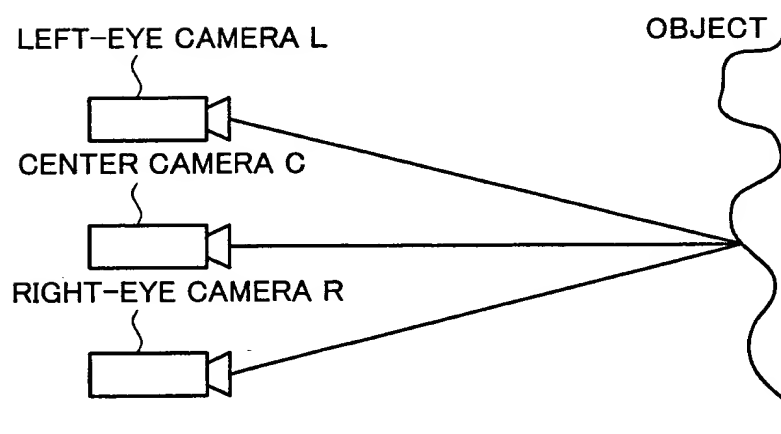
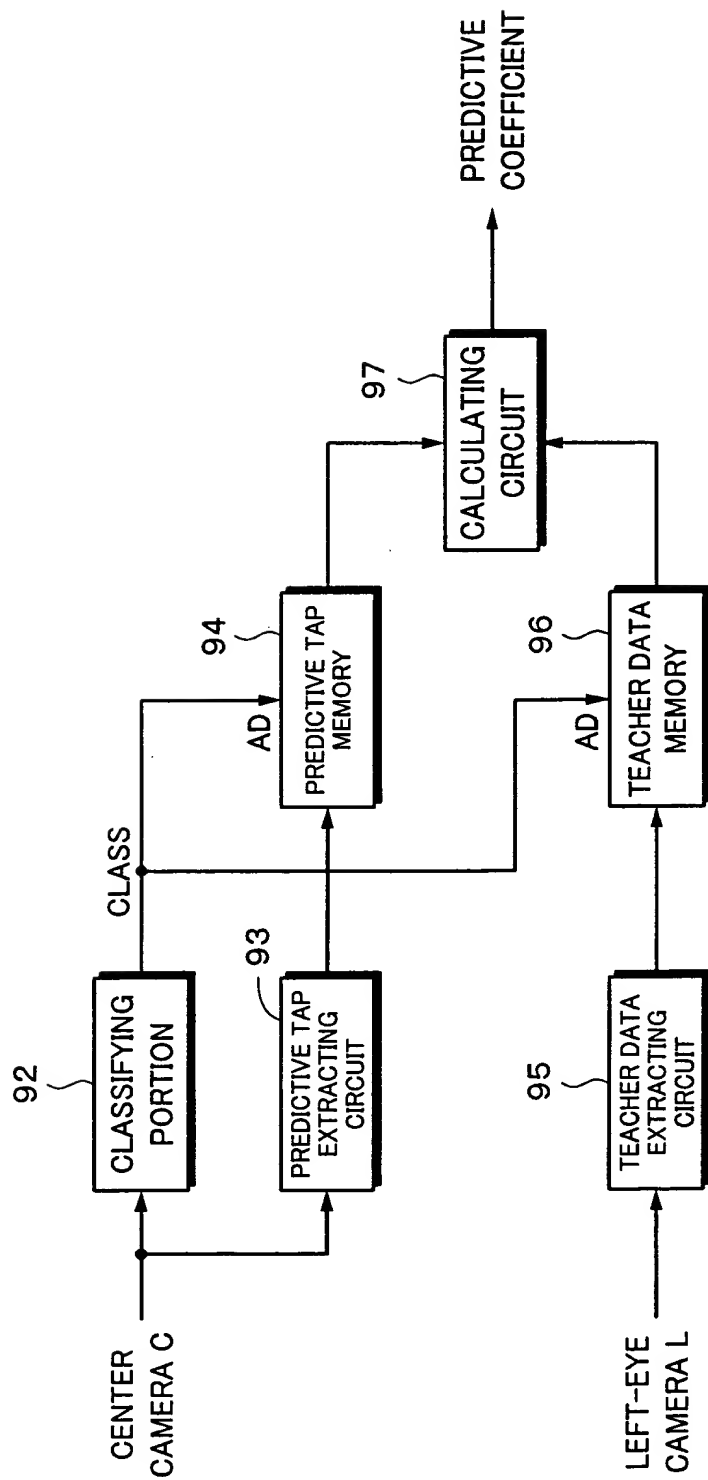
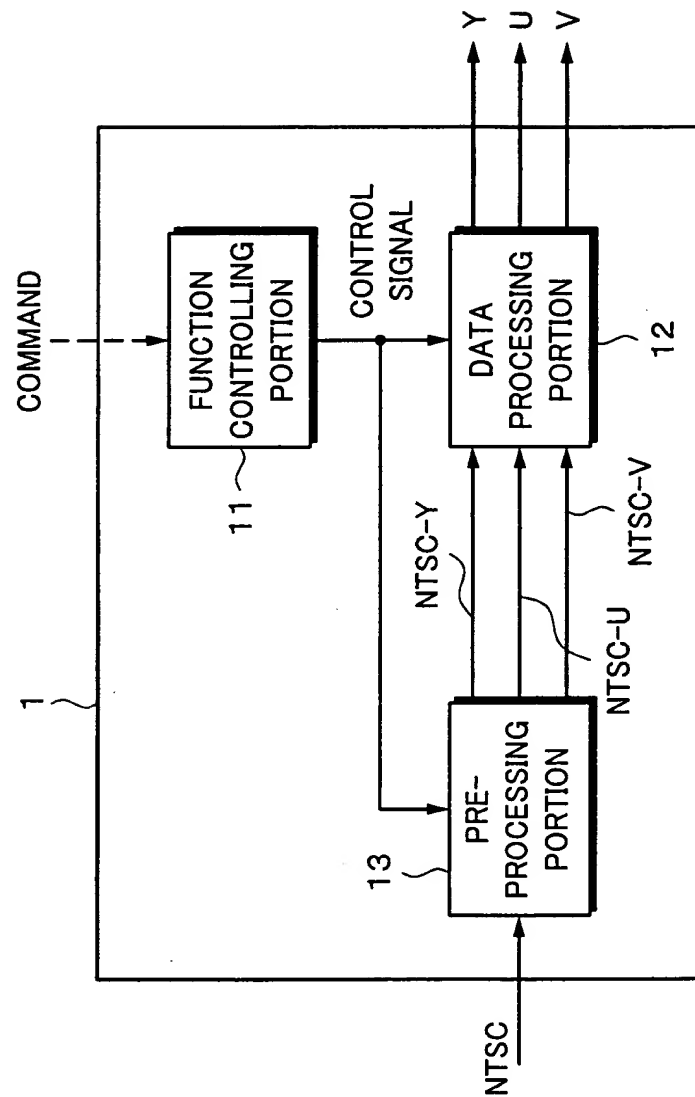
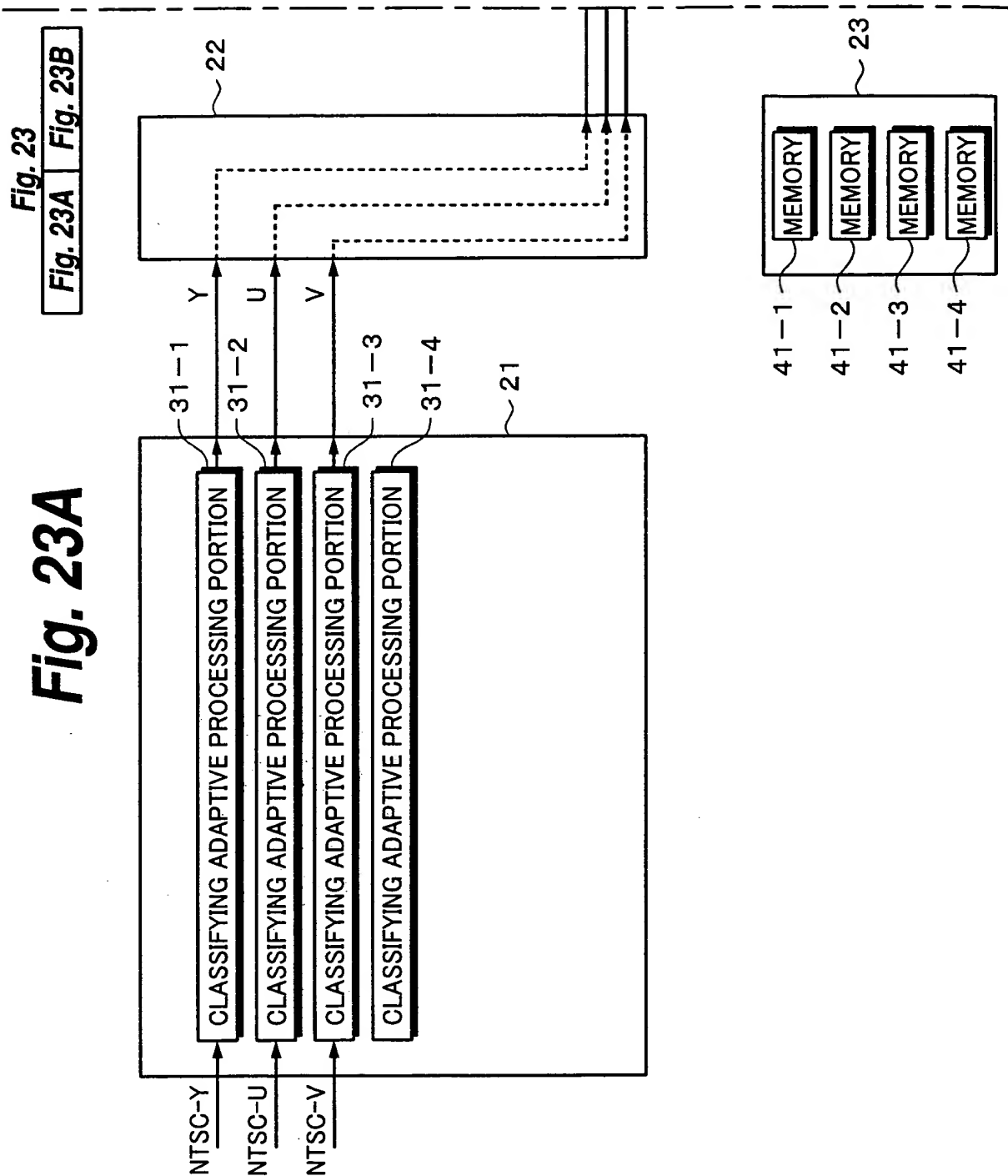
**Fig. 19****Fig. 20**

Fig. 21





**Fig. 22**



**Fig. 23B**

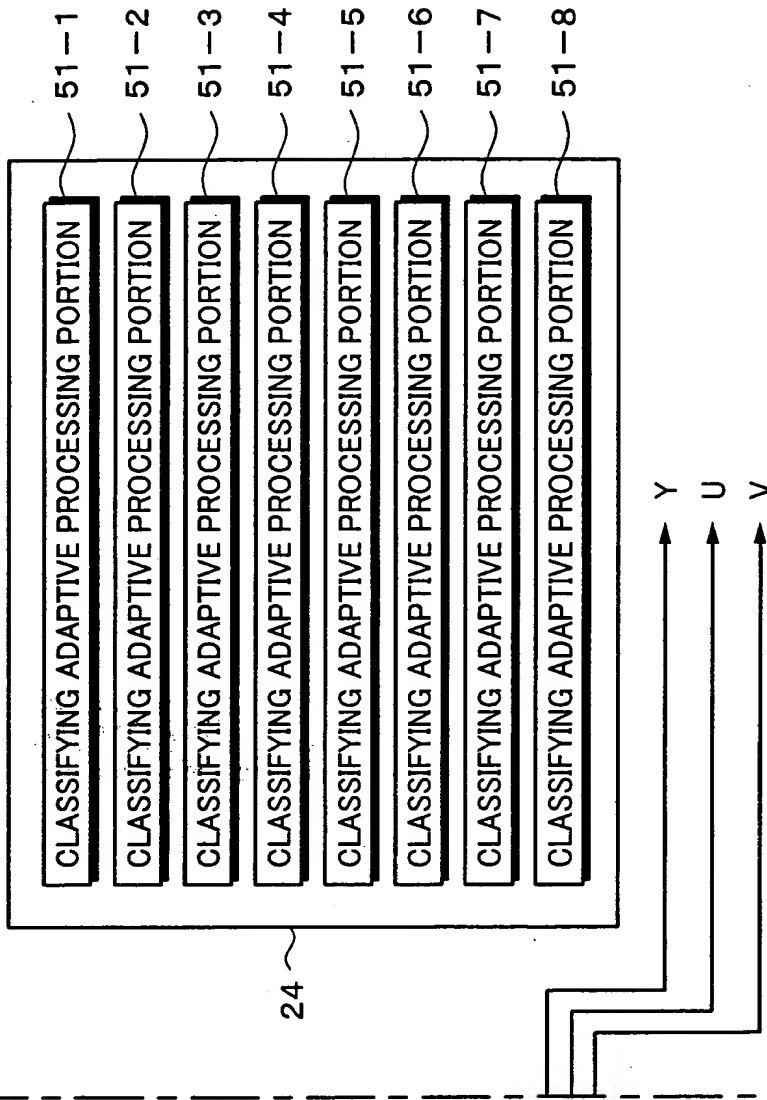
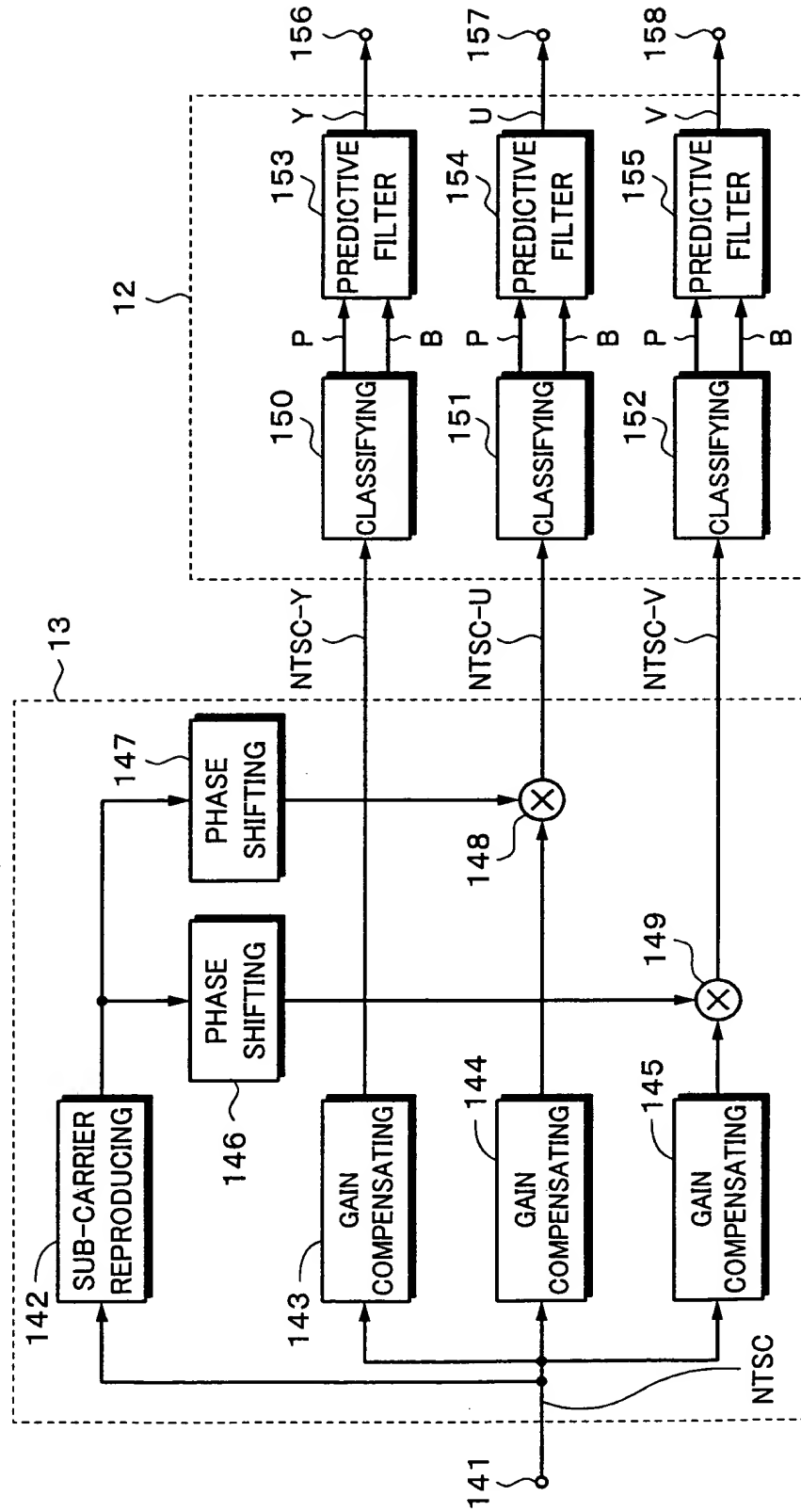
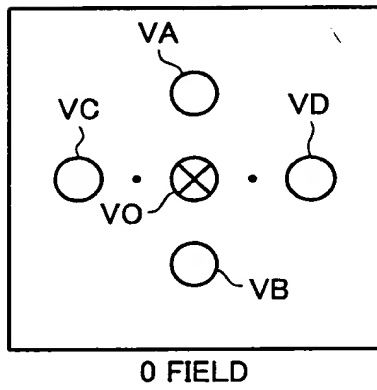


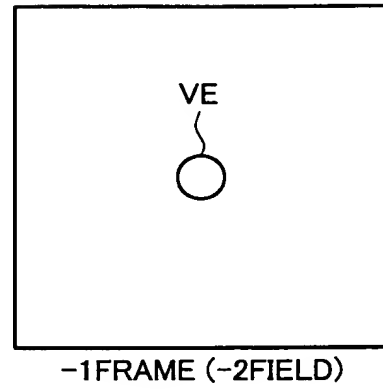
Fig. 24



**Fig. 25A**



**Fig. 25B**



**Fig. 26**

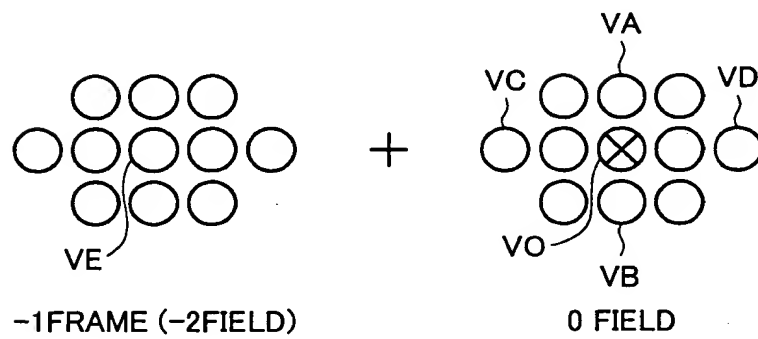


Fig. 27

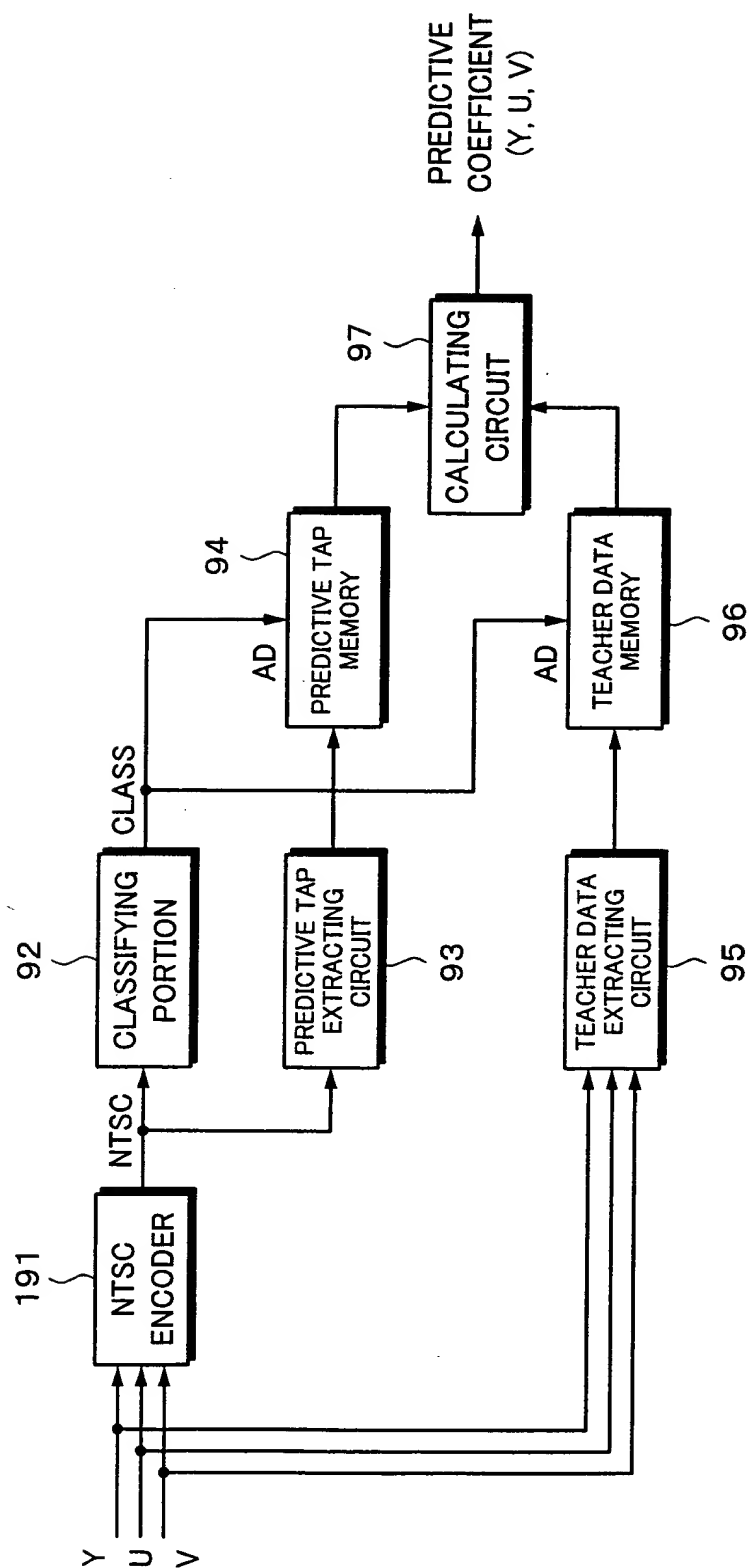


Fig. 28

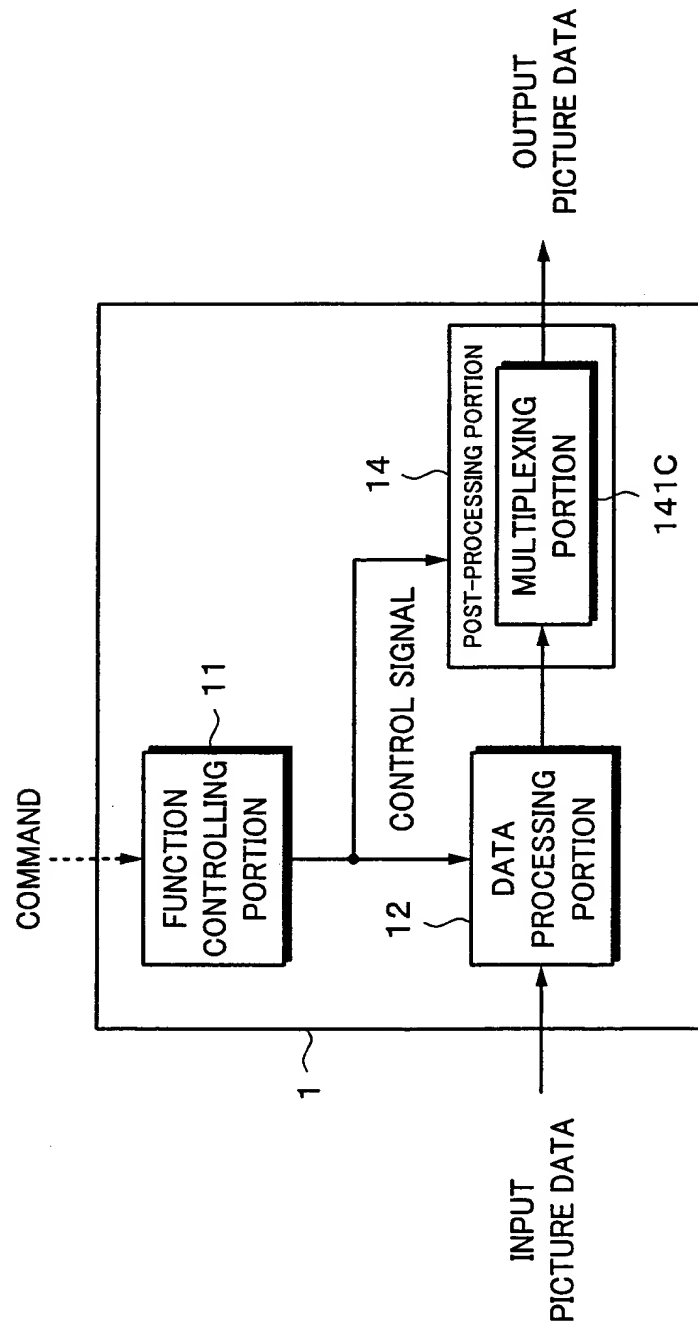
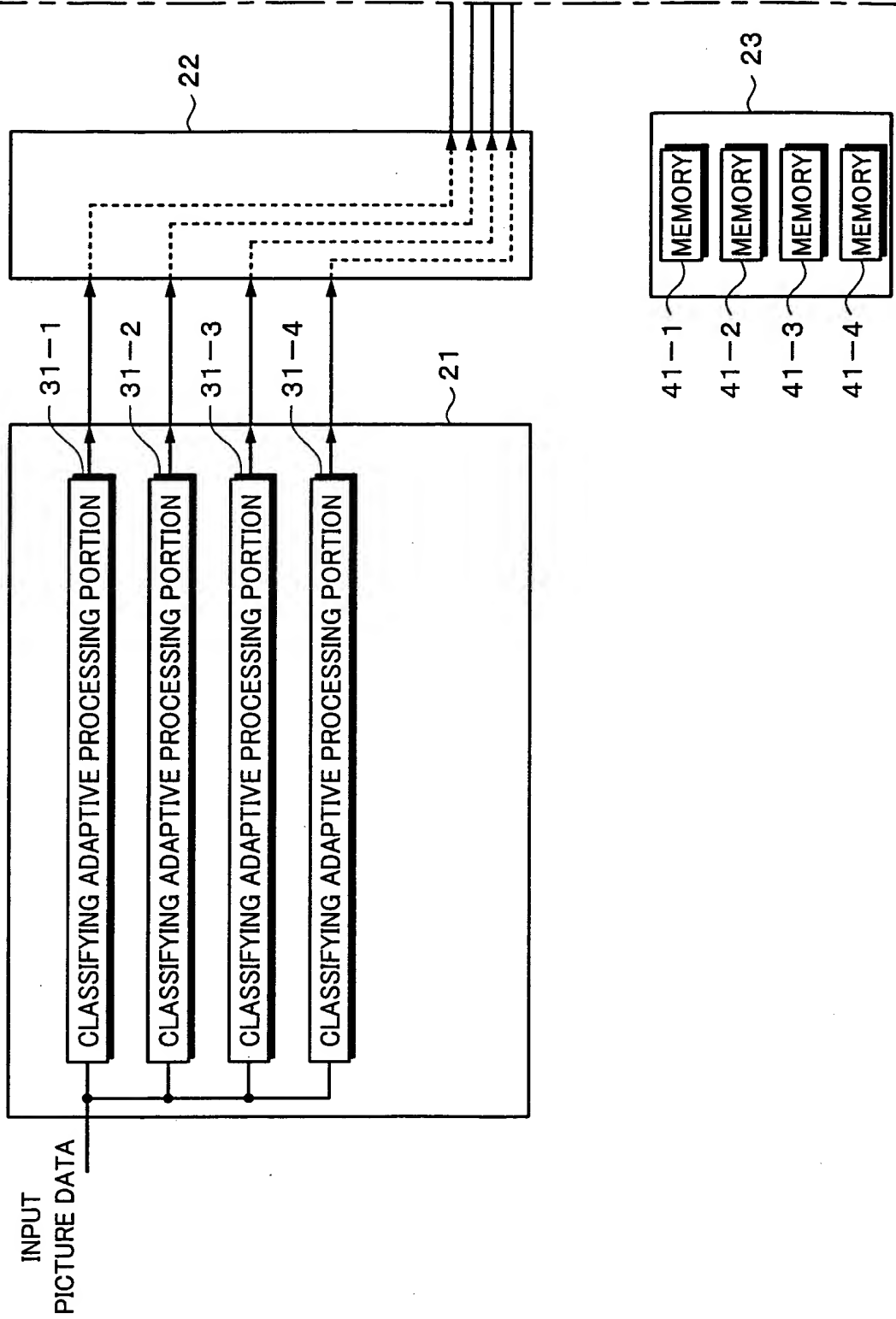


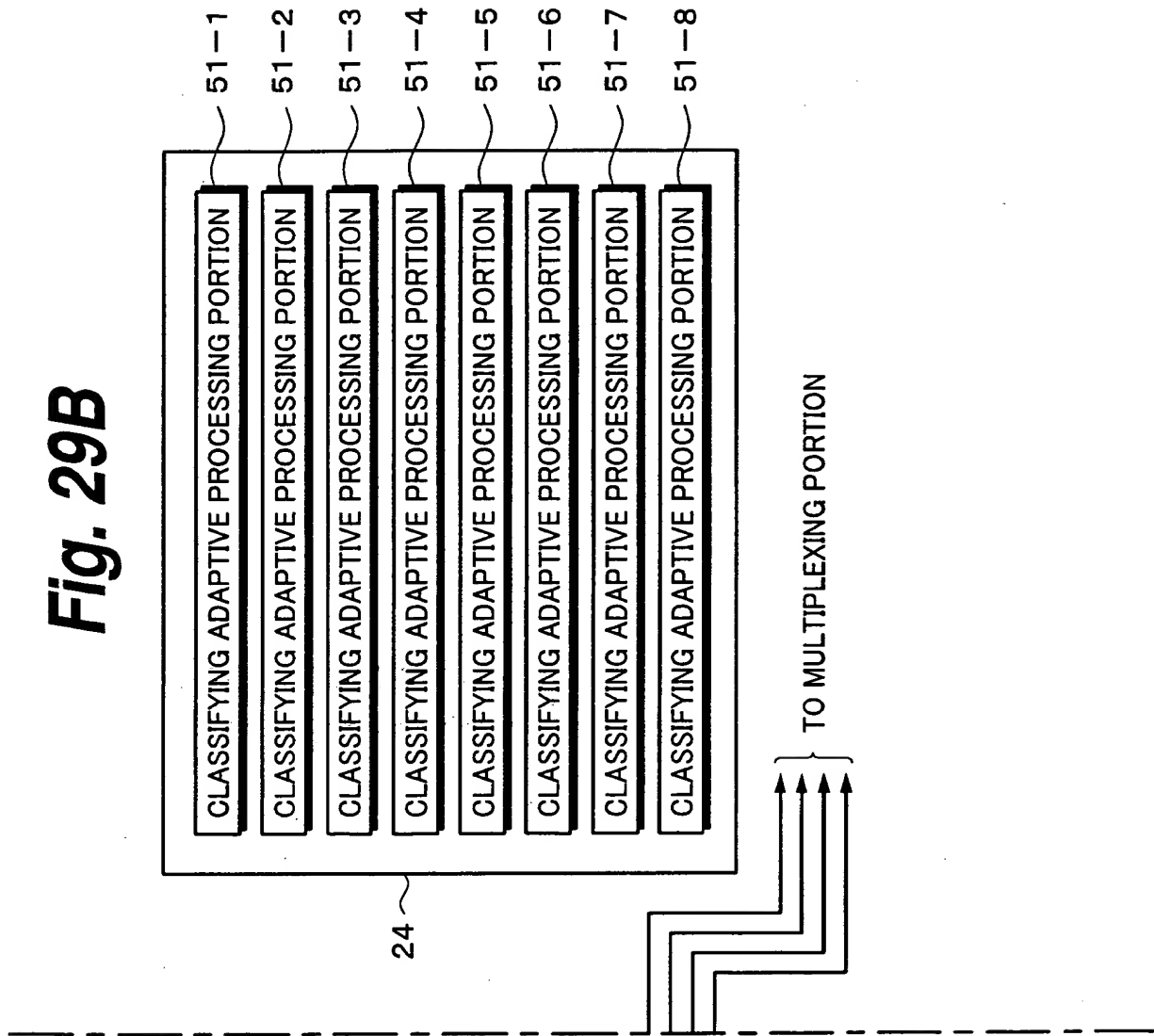
Fig. 29A

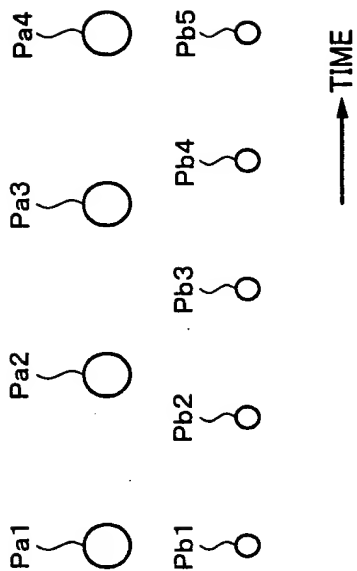
Fig. 29A Fig. 29B





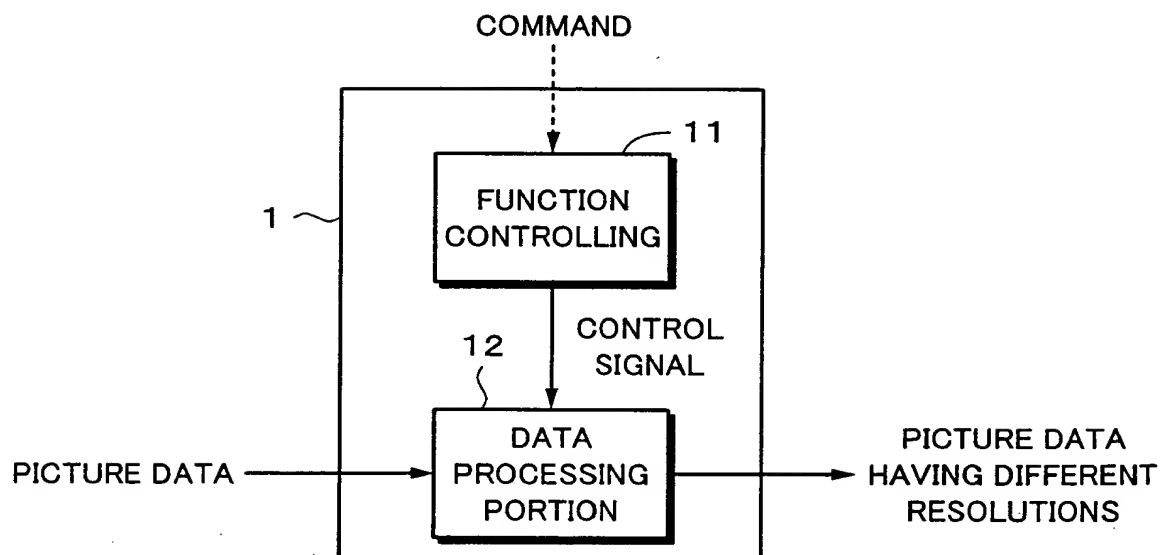
**Fig. 29B**





**Fig. 30A**

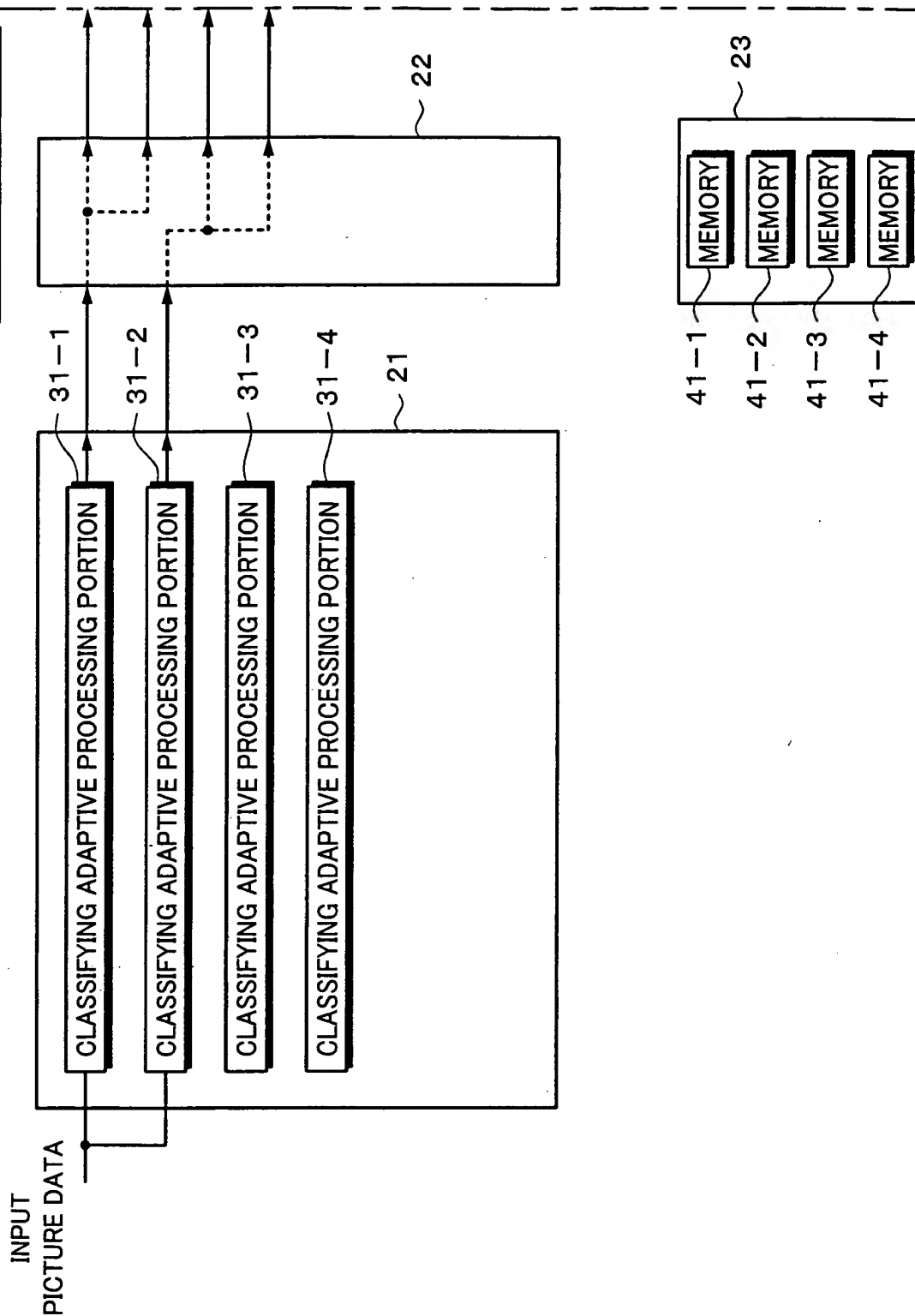
**Fig. 30B**

**Fig. 31**

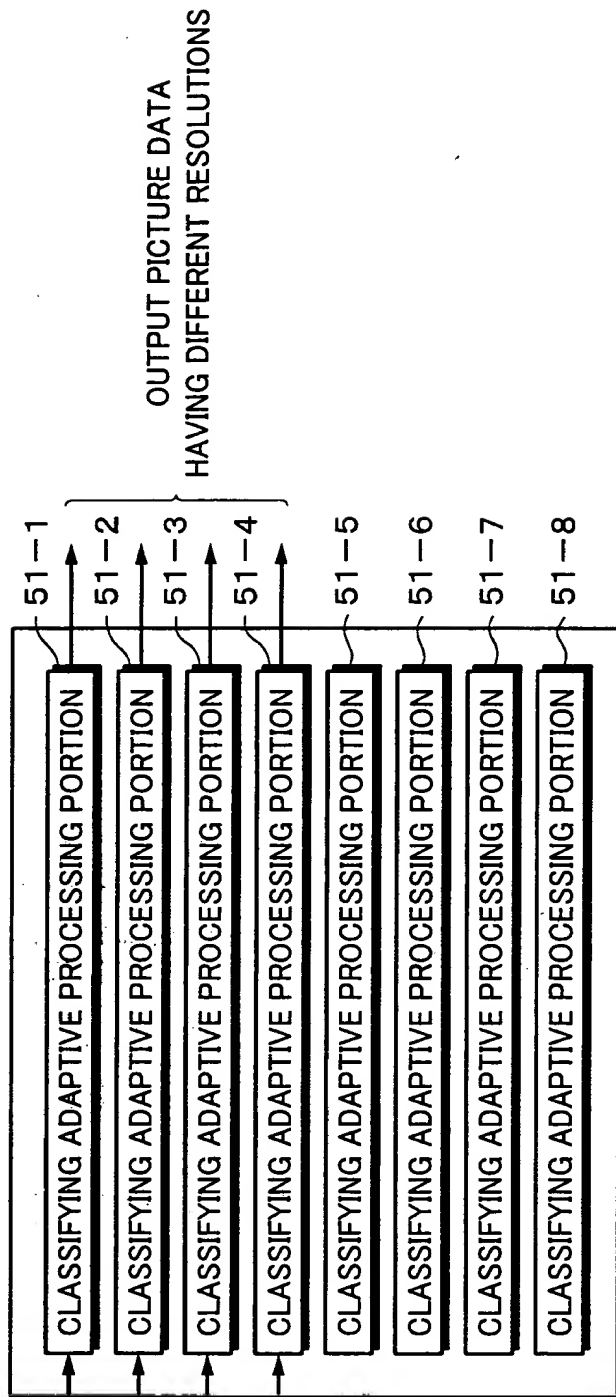
**Fig. 32A**

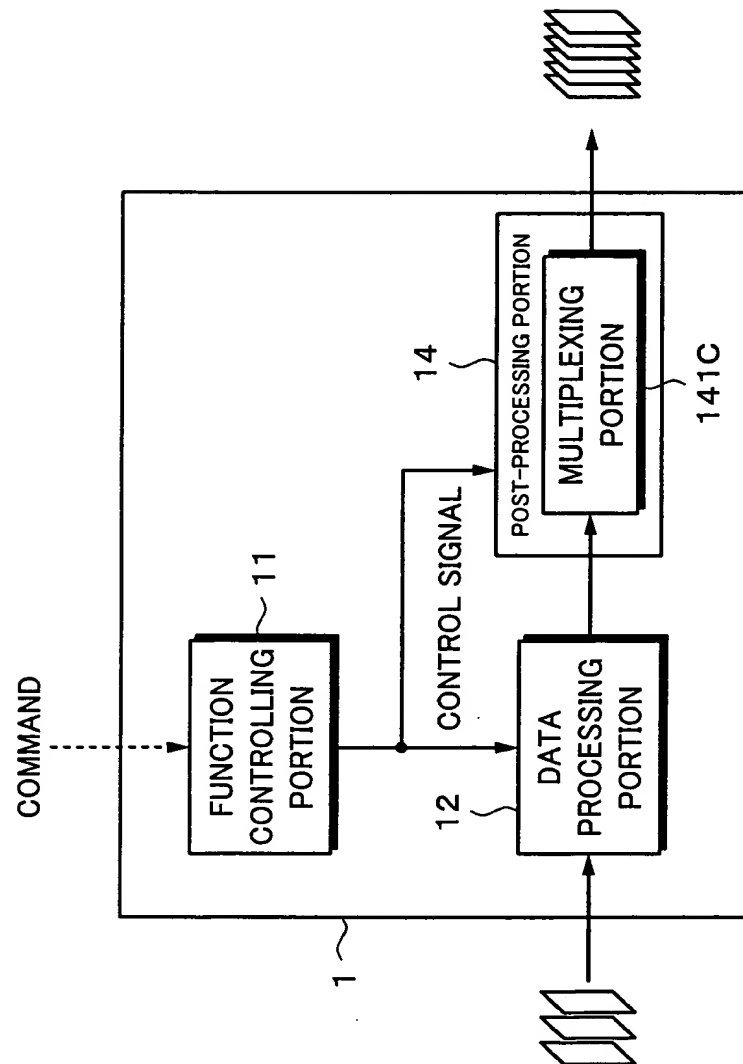
**Fig. 32**

**Fig. 32A Fig. 32B**



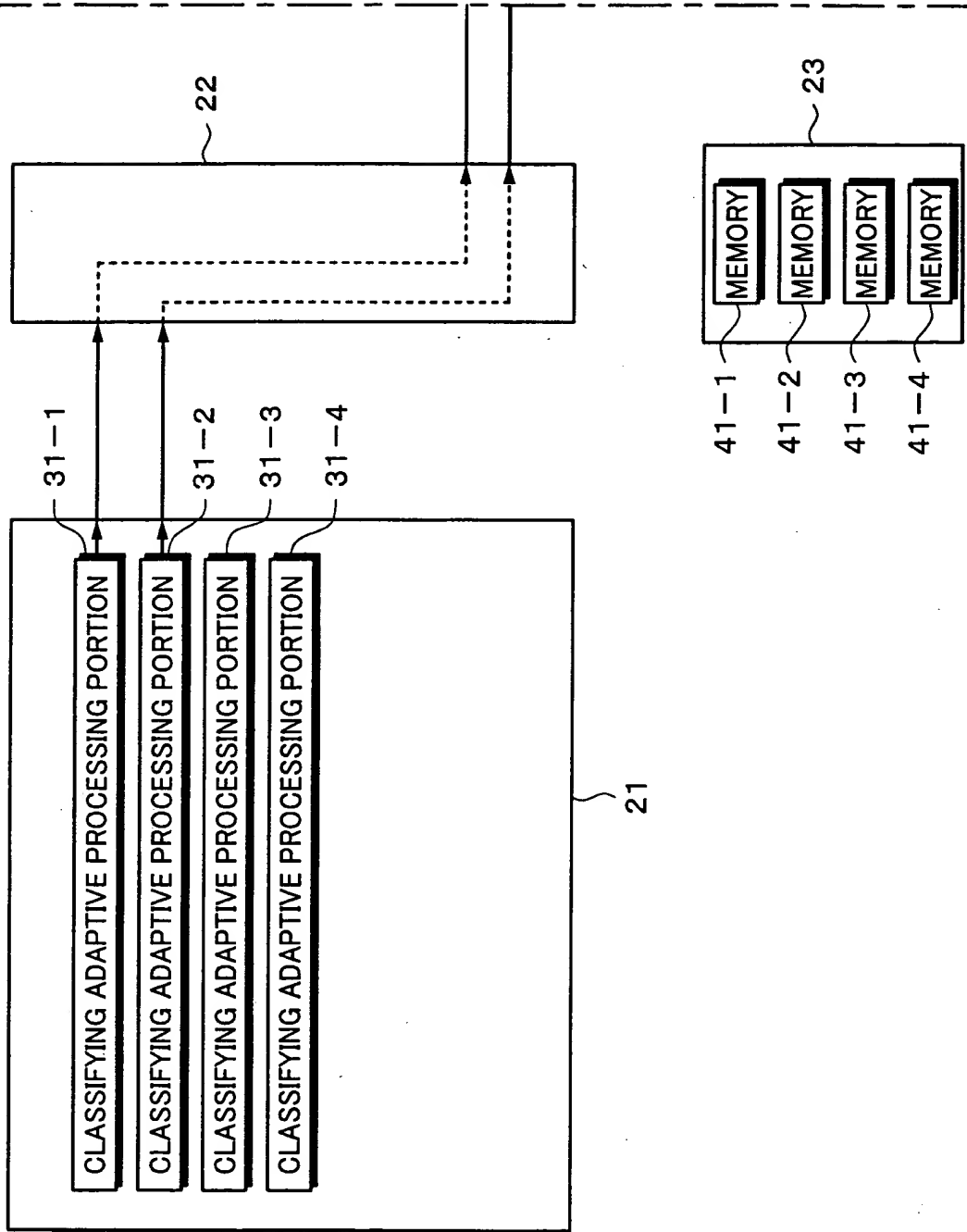
**Fig. 32B**



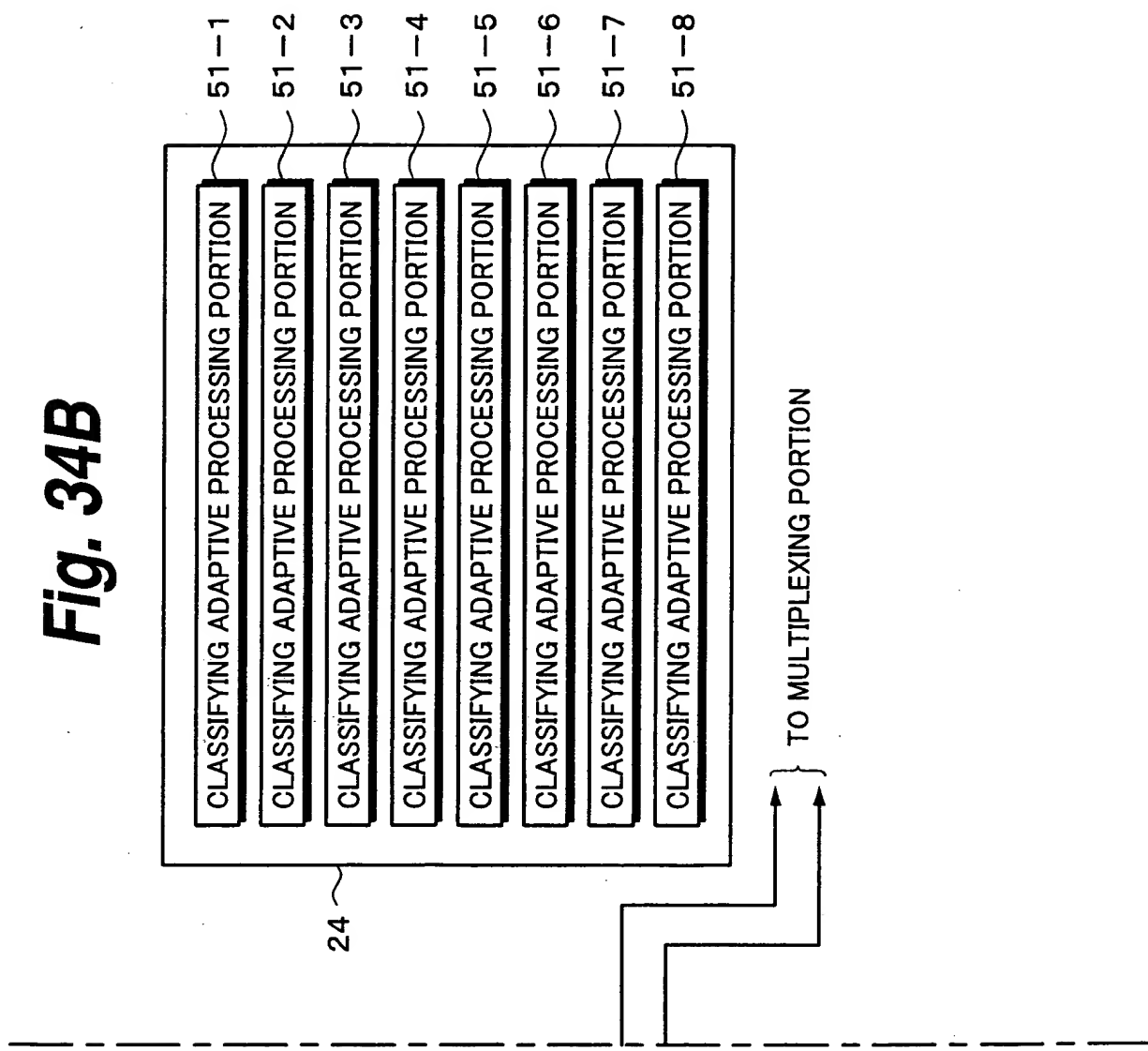
**Fig. 33**

**Fig. 34A**

**Fig. 34**  
**Fig. 34A** **Fig. 34B**



**Fig. 34B**





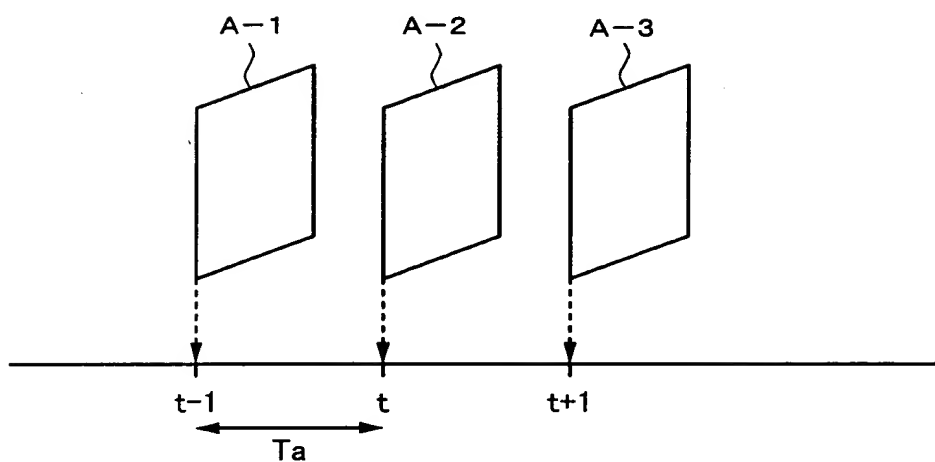
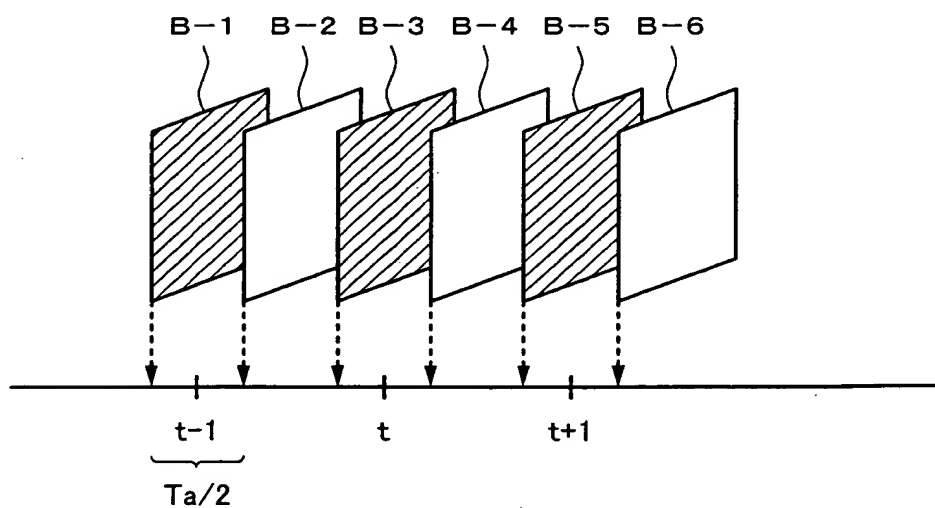
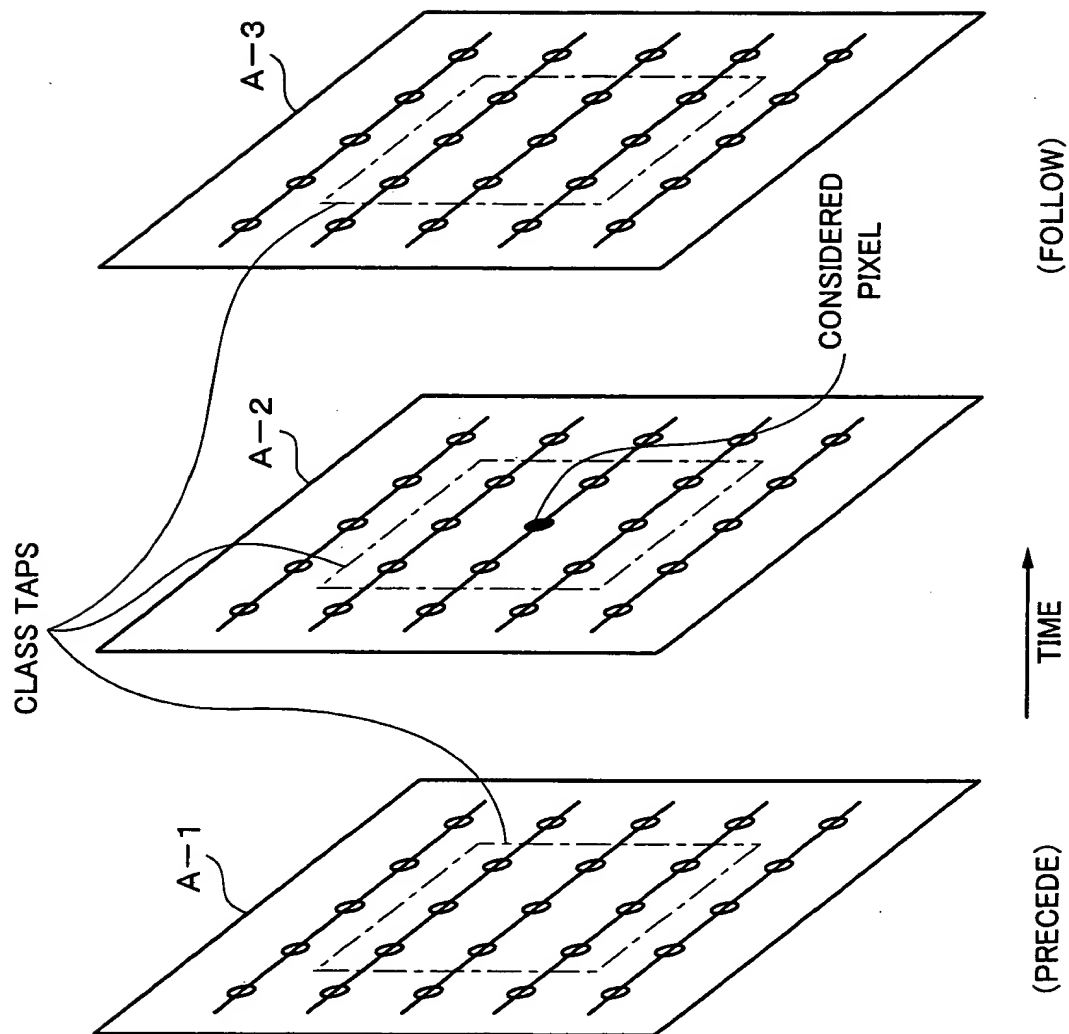
**Fig. 35A****Fig. 35B**

Fig. 36



## DESCRIPTION OF REFERENCE NUMERALS

- 1 DATA PROCESSING APPARATUS
- 11 FUNCTION CONTROLLING PORTION
- 12 DATA PROCESSING PORTION
- 13 PRE-PROCESSING PORTION
- 14 POST-PROCESSING PORTION
- 21 INPUT DATA PROCESSING PORTION
- 22 SELECTOR
- 23 INTERMEDIATE DATA PROCESSING PORTION
- 24 OUTPUT DATA PROCESSING PORTION
- 31, 51 CLASSIFYING ADAPTIVE PROCESSING PORTION
- 41 MEMORY